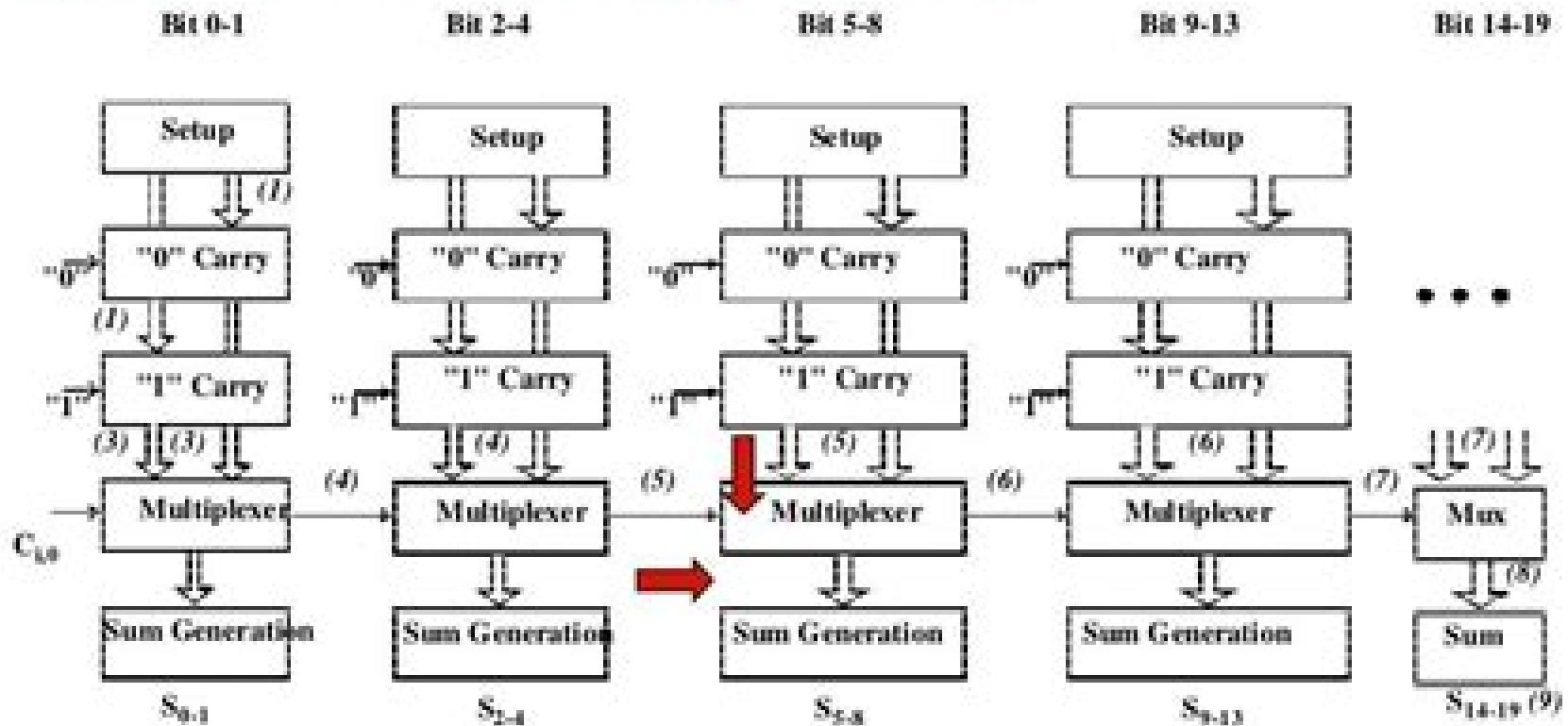


# Square Root Carry Select



$$t_{add} = t_{setup} + P \cdot t_{carry} + (\sqrt{2N}) t_{mux} + t_{sum}$$

$N$ -bit adder with  $P$  stages, 1<sup>st</sup> stage adds  $M$  bits

$$\rightarrow N = P^2/2 \Rightarrow P \approx \sqrt{2N}$$

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**Raffaela Di Napoli**

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*Proceedings of the ASP-DAC ... Asia and South Pacific Design Automation Conference ,1998*    **Proceedings** ,1998  
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**Using Binary Excess Converter** Prajakta Wasekar,U.M. Gokhale,2015-01-28 Scientific Essay from the year 2012 in the subject Electrotechnology Priyadarshini College of Engineering Nagpur language English abstract Adders are one of the widely used digital components in digital integrated circuit design Addition is the basic operation used in almost all computational systems Therefore the efficient implementation and design of arithmetic units requires the binary adder structures to be implemented in an equally efficient manner A ripple carry adder has smaller area but less speed A carry look ahead adder is faster though its area requirements are high Carry select adders CSLA lie in middle In this work a novel carry select adder using Binary Excess Converter BEC is proposed It provides good compromise between cost and performance thereby establishing a proper trade off between time and area complexities In this work Tanner EDA is used for the comparison of all adders Ripple carry adder Bitwise carry select adder Square root carry select adder proposed carry select adder using BEC    **ARITH-15 2001** Neil Burgess,2001 The proceedings from the June 2001 conference in Vail Colorado feature 30 papers on binary strings multiplication and exponentiation cryptography division and square root elementary functions and rounding number systems floating high points addition logarithmic number systems and on line arithmetic An abstract of the keynote speech offers a processor architect s perspective on computer arithmetic And a reprint of Knowles s A Family of Adders which was mis printed in the proceedings from the previous conference is included Contributors represent 11 countries Name index only c Book News Inc    **Proceedings of the 33rd Midwest Symposium on Circuits and Systems** Ronald H. Johnston,B Nowrouzian,L. E Turner,1991    Proceedings Milos D. Ercegovac,Earl Swartzlander,1989  
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**IBM Journal of Research and Development**, 1999

**Computer Arithmetic Systems** Amos R. Omondi, 1994

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web the following information relates to the capital structure of c ltd a publicly listed company as at 1 january 20x6 and events and net profit of c ltd for the year ended 31 december