

8086 INSTRUCTION SET

OPCODE	DESCRIPTION	OPCODE	DESCRIPTION	OPCODE	DESCRIPTION
AAA	ASCII adjust addition	JNAE	label Jump if not above or equal	POPF	Push flags onto stack
AAD	ASCII adjust division	JNB	label Jump if not below	ROL	dr,es: Push left through carry
AMM	ASCII adjust multiply	JNBE	label Jump if below or equal	ROR	dr,es: Push right through carry
ASM	ASCII adjust subtraction	JNC	label Jump if no carry	REP	Repeat string operation
ADC	dr,es Add with carry	JNE	label Jump if not equal	REPE	Repeat while equal
ADD	dr,es Add	JNG	label Jump if not greater	REPZ	Repeat while zero
AND	dr,es Logical AND	JNGE	label Jump if not greater or equal	REPNE	Repeat while not zero
CALL	proc Call a procedure	JNL	label Jump if not less	REPNZ	Repeat while not zero
CBW	Convert byte to word	JNLE	label Jump if not less or equal	RET	(pop) Return from procedure
CLC	Clear carry flag	JNO	label Jump if not overflow	ROL	dr,es: Rotate left
CLD	Clear direction flag	JNP	label Jump if not parity	ROR	dr,es: Rotate right
CLI	Clear interrupt flag	JNS	label Jump if not sign	STC	Store All into flags
CMC	Complement carry flag	JPO	label Jump if overflow	SHL	dr,es: Shift arithmetic left
CMF	dr,es Compare	JPE	label Jump if parity odd	SHL	dr,es: Shift logical left
CMPS	[dr,es] Compare string	JPS	label Jump if parity even	SHR	dr,es: Shift arithmetic right
CMPSB	- bytes	JSC	label Jump if sign	SUB	dr,es: Subtract with borrow
CMPSW	- words	JZ	label Jump if zero	SCAS	(dr) Scan string
CWD	Convert word to double word	LAHF	Load AH from flags	SCASB	- byte
DAA	Decimal adjust addition	LDS	dr,es Load pointer using DS	SCASW	- word
DAS	Decimal adjust subtraction	LEA	dr,es Load effective address	SHR	dr,es: Shift logical right
DEC	dt Decrement	LGS	dr,es Load pointer using GS	STC	Set carry flag
DIV	es Unsigned divide	LOCK	Lock bus	STD	Set direction flag
ESC	code,es Escape	LODS [es]	Load string	STI	Set interrupt flag
HLT	Halt	LODSB	- bytes	STOS	(dt) Store string
IDIV	es Integer divide	LODSW	- words	STOVB	- byte
IMUL	es Integer multiply	LOOP	label Loop	STOVM	- word
IN	es,port Input from port	LOOPB	label Loop if equal	SUB	dr,es Subtraction
INC	dt Increment	LOOPD	label Loop if zero	TEST	dr,es Test (logical AND)
INT	Type Interrupt	LOOPNE	label Loop if not equal	WAIT	Wait for interrupt
INTO	Interrupt if overflow	LOOPNZ	label Loop if not zero	XCHG	dr,es Exchange
IRET	Return from interrupt	MOV	dr,es Move	XLAT	Table Translate
JA	label Jump if above	MOV	(dr,es) Move string	XLATB	-
JAE	label Jump if above or equal	MOV	- bytes	XOR	dr,es Logical exclusive OR
JB	label Jump if below	MOV	- words		
JBE	label Jump if below or equal	MUL	es Unsigned multiply		
JC	label Jump if carry	MOD	dr Negate		
JCXZ	label Jump if CX is zero	MOP	No operation		
JE	label Jump if equal	NOT	dr Logical NOT		
JG	label Jump if greater	OR	dr,es Logical OR		
JGE	label Jump if greater or equal	OUT	port,es Output to port		
JL	label Jump if less	POP	dr Pop word off stack		
JLE	label Jump if less or equal	POPF	dr Pop flags off stack		
JNP	label Jump	POPF	dr Push word onto stack		
JNA	label Jump if not above	POPM	es Push flags onto stack		

Notes:

dt - destination

es - source

label - may be near or far address

label - base address

8086 Instruction Set Machine Code

Katrin Zwirglmaier

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Fundamentals of Digital Logic and Microcomputer Design M. Rafiquzzaman, 2005-07-08 Fundamentals of Digital Logic and Microcomputer Design has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers. In this Fifth Edition the author focuses on computer design at three levels: the device level, the logic level, and the system level. Basic topics are covered such as number systems and Boolean algebra, combinational and sequential logic design, as well as more advanced subjects such as assembly language programming and microprocessor-based system design. Numerous examples are provided throughout the text. Coverage includes Digital circuits at the gate and flip flop levels, Analysis and design of combinational and sequential circuits, Microcomputer organization, architecture, and programming concepts, Design of computer instruction sets, CPU memory and I/O System design features associated with popular microprocessors from Intel and Motorola. Future plans in microprocessor development. An instructor's manual is available upon request. Additionally, the accompanying CD ROM contains step-by-step procedures for installing and using Altera Quartus II software, MASM 6.11, 8086 and 68asm sim 68000, provides valuable simulation results via screen shots. Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems.

BTEC Nationals - IT

Practitioners Howard Anderson, Sharon Yull, 2012-05-16 Full coverage of all 6 core units for the new BTEC National specifications from Edexcel. BTEC Nationals IT Practitioners is a new course text written specifically to cover the compulsory core units of the new BTEC Nationals specifications which are replacing the current BTEC National Computing scheme in 2002. Students following all National Certificate and Diploma programmes within the new scheme will find this book essential reading as it covers the core material they will be following through the duration of their course. Knowledge check questions and activities are included throughout, resulting in a clear and straightforward text which encourages independent study. The coverage is designed to cater for students following a General ICT Systems Support or Software Development course.

Answers to numerical problems are included and further support material is available on the Newnes website. Advance Microprocessor A.K. Gautam, Each topic is well explained by illustration and photographs. The book covers basic microprocessors to advanced processors in a consistent progression from theoretical concept to design considerations. The operation of various microprocessors is described with the help of pin diagram, functional diagram, and timing diagrams. A large number of working programs, problems, and the each chapter are summarized in the end.

PIC Microcontrollers

Martin P. Bates, 2004-06-09 The use of microcontroller-based solutions to everyday design problems in electronics is the most important development in the field since the introduction of the microprocessor itself. The PIC family is established as the number one microcontroller at an introductory level. Assuming no prior knowledge of microprocessors, Martin Bates provides a comprehensive introduction to microprocessor systems and applications, covering all the basic principles of

microelectronics Using the latest Windows development software MPLAB the author goes on to introduce microelectronic systems through the most popular PIC devices currently used for project work both in schools and colleges as well as undergraduate university courses Students of introductory level microelectronics including microprocessor microcontroller systems courses introductory embedded systems design and control electronics will find this highly illustrated text covers all their requirements for working with the PIC Part A covers the essential principles concentrating on a systems approach The PIC itself is covered in Part B step by step leading to demonstration programmes using labels subroutines timer and interrupts Part C then shows how applications may be developed using the latest Windows software and some hardware prototyping methods The new edition is suitable for a range of students and PIC enthusiasts from beginner to first and second year undergraduate level In the UK the book is of specific relevance to AVCE as well as BTEC National and Higher National programmes in electronic engineering A comprehensive introductory text in microelectronic systems written round the leading chip for project work Uses the latest Windows development software MPLAB and the most popular types of PIC for accessible and low cost practical work Focuses on the 16F84 as the starting point for introducing the basic architecture of the PIC but also covers newer chips in the 16F8X range and 8 pin mini PICs *PC Mag*, 1988-11-15 PCMag com is a leading authority on technology delivering Labs based independent reviews of the latest products and services Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology [A Glossary of Computing Terms](#) British Computer Society, 1998 This authoritative glossary provides explanations for over 2 400 computing terms covering all aspects of computing from programming languages to personnel from architecture to applications from storage to systems software **Programming with Turbo C** S. Scott Zimmerman, Beverly B. Zimmerman, 1989

The 80386, 80486, and Pentium Processors Walter A. Triebel, 1998 This book is the first to concentrate on all 32 bit microprocessors and the pentium This comprehensive exploration of microprocessor technology introduces core concepts techniques and applications using the 80386 80486 and Pentium processors putting equal emphasis on assembly language software programming and microcomputer hardware interfacing The second part of this book presents software memory circuits I/O and peripherals The third part consists of PC AT business interfacing testing troubleshooting and the pentium For anyone interested in Microprocessor Technology **Advanced Microprocessor & Microcontrollers** S. K. Venkata Ram, 2004 [Using Assembly Language](#) Allen L. Wyatt, Sr., Allen Wyatt, 1992 The revised edition to cover the most recent releases of both Microsoft's Macros Assembler and Borland's Turbo Assembler Written from a programmer's perspective this power packed text explains how to use the most popular assemblers linkers and debuggers Includes a comprehensive reference section *PC Mag*, 1988-08 PCMag com is a leading authority on technology delivering Labs based independent reviews of the latest products and services Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology **C Programming on the IBM PC** S. Scott Zimmerman, 1991

Assembly Language Programming for the Intel 80XXX Family William B. Giles, 1991 Considers assembly programming language for the entire 80XXX family and deals with such topics as how addresses are computed what the linker and loader do and why the 80386 is a significant advance It includes end of section exercises program diagrams and examples of working programs **Assembly Language for Intel-based Computers** Kip R. Irvine, 2007 This widely used fully updated assembly language book provides basic information for the beginning programmer interested in computer architecture operating systems hardware manipulation and compiler writing Uses the Intel IA 32 processor family as its base showing how to program for Windows and DOS Is written in a clear and straightforward manner for high readability Includes a companion CD ROM with all sample programs and Microsoft^{reg} Macro Assembler Version 8 along with an extensive companion Website maintained by the author Covers machine architecture processor architecture assembly language fundamentals data transfer addressing and arithmetic procedures conditional processing integer arithmetic strings and arrays structures and macros 32 bit Windows programming language interface disk fundamentals BIOS level programming MS DOS programming floating point programming and IA 32 instruction encoding For embedded systems programmers and engineers communication specialists game programmers and graphics programmers

Microprocessors Gene A. Streitmatter, Vito Fiore, 1979 *The Waite Group's Microsoft Macro Assembler Bible* Nabajyoti Barkakati, Randall Hyde, 1992 This book explores all of the new features including improved data types support enhanced macro capabilities single pass operation and a low level optimizer Also any programmer using BASIC C FORTRAN will now be able to move their programs easily into the DOS environment with the excellent tutorial and reference material **The 80286 Microprocessor** Walter A. Triebel, Avtar Singh, 1990 For an advanced course in 16 Bit micros Intel chip Incorporates hardware software and interfacing techniques geared to the 80286 Optional lab assignments are presented at the end of each chapter Includes step by step examples and practice problems **Computer Organization and Assembly Language Programming for IBM PCs and Compatibles** Michael Thorne, 1991 This comprehensive book provides an up to date guide to programming the Intel 8086 family of microprocessors emphasizing the close relationship between microprocessor architecture and the implementation of high level languages **Robots 12 and Vision '88 Conference**, 1988 **Electronic Design**, 1983

Unveiling the Power of Verbal Artistry: An Mental Sojourn through **8086 Instruction Set Machine Code**

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