

## ARM INSTRUCTION SET

OPCODE		DESCRIPTION	JMnemonic	label	Jump if not above or equal	FUNCTION	Push flags onto stack
AAA		ASCII adjust addition	JNB	label	Jump if not below	ROL	dt, cnt Rotate left through carry
AAD		ASCII adjust division	JNBE	label	Jump if below or equal	ROR	dt, cnt Rotate right through carry
AAM		ASCII adjust multiply	JNC	label	Jump if no carry	REP	Repeat string operation
AAS		ASCII adjust subtraction	JNE	label	Jump if not equal	REPE	Repeat while equal
ADC	dt, src	Add with carry	JNG	label	Jump if not greater	REPZ	Repeat while zero
ADD	dt, src	Add	JNGE	label	Jump if not greater or equal	REPNE	Repeat while not equal
AND	dt, src	Logical AND	JNL	label	Jump if not less	REPNI	Repeat while not zero
CALL	proc	Call a procedure	JNLE	label	Jump if not less or equal	RET	[pop] Return from procedure
CWM		Convert byte to word	JNI	label	Jump if not zero	ROL	dt, cnt Rotate left
CLC		Clear carry flag	JNO	label	Jump if not overflow	ROR	dt, cnt Rotate right
CML		Clear direction flag	JNP	label	Jump if not parity	SASF	Store AS into flags
CLI		Clear interrupt flag	JNS	label	Jump if not sign	SAL	dt, cnt Shift arithmetic left
CMC		Complement carry flag	JO	label	Jump if overflow	SRL	dt, cnt Shift logical left
CMF	dt, src	Compare	JPO	label	Jump if parity odd	SAR	dt, cnt Shift arithmetic right
CMPS	[dt, src]	Compare string	JP	label	Jump if parity	SSB	dt, src Subtract with borrow
CMPSB	-	- bytes	JPE	label	Jump if parity even	SCAS	[dt] Scan string
CMPSW	-	- words	JS	label	Jump if sign	SCASB	- byte
CWD		Convert word to double word	JZ	label	Jump if zero	SCASW	- word
DAA		Decimal adjust addition	LASF		Load AS from flags	SHR	dt, cnt Shift logical right
DAS		Decimal adjust subtraction	LDS	dt, src	Load pointer using DS	STC	Set carry flag
DEC	dt	Decrement	LEA	dt, src	Load effective address	STD	Set direction flag
DIV	src	Unsigned divide	LES	dt, src	Load pointer using ES	STI	Set interrupt flag
ESC	code, src	Escape	LOCK		Lock bus	STOS	[dt] Store string
HLT		Halt	LODS	[src]	Load string	STOSB	- byte
IDIV	src	Integer divide	LODSB	-	- bytes	STOSW	- word
IMUL	src	Integer multiply	LODSW	-	- words	SUB	dt, src Subtraction
IN	src, port	Input from port	LOOP	label	Loop	TEST	dt, src Test (logical AND)
INC	dt	Increment	LOOPE	label	Loop if equal	WAIT	Wait for \$087
INT	type	Interrupt	LOOPE	label	Loop if zero	XCHG	dt, src Exchange
INTO		Interrupt if overflow	LOOPE	label	Loop if not equal	XLAT	table Translate
INUT		Return from interrupt	LOOPE	label	Loop if not zero	XLATB	-
JL	label	Jump if above	MOV	dt, src	Move	XOR	dt, src Logical exclusive OR
JAE	label	Jump if above or equal	MOVSB	[dt, src]	Move string		
JB	label	Jump if below	MOVSB	-	- bytes		
JBE	label	Jump if below or equal	MOVSW	-	- words		
JC	label	Jump if carry	MUL	src	Unsigned multiply		
JCXZ	label	Jump if CX is zero	MOS	dt	Repete		
JE	label	Jump if equal	NOF		No operation		
JG	label	Jump if greater	NOR	dt	Logical NOR		
JGE	label	Jump if greater or equal	OR	dt, src	Logical OR		
JL	label	Jump if less	OUT	port, src	Output to port		
JLE	label	Jump if less or equal	POP	dt	Pop word off stack		
JMP	label	Jump	POPF		Pop flags off stack		
JNA	label	Jump if not above	PUSH	src	Push word onto stack		

Notes:

dt - destination

src - source

label - may be near or far address

label - near address

# 8086 Instruction Set Machine Code

**Katrin Zwirgmaier**



## **8086 Instruction Set Machine Code:**

Fundamentals of Digital Logic and Microcomputer Design M. Rafiquzzaman, 2005-07-08 Fundamentals of Digital Logic and Microcomputer Design has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers. In this Fifth Edition the author focuses on computer design at three levels: the device level, the logic level, and the system level. Basic topics are covered such as number systems and Boolean algebra, combinational and sequential logic design, as well as more advanced subjects such as assembly language programming and microprocessor based system design. Numerous examples are provided throughout the text. Coverage includes Digital circuits at the gate and flip flop levels, Analysis and design of combinational and sequential circuits, Microcomputer organization, architecture and programming concepts, Design of computer instruction sets, CPU memory and I/O System design, features associated with popular microprocessors from Intel and Motorola, Future plans in microprocessor development. An instructor's manual is available upon request. Additionally, the accompanying CD-ROM contains step by step procedures for installing and using Altera Quartus II software, MASM 6.11, 8086 and 68asm, sim68000 provides valuable simulation results via screen shots. Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems. **BTEC Nationals - IT**

**Practitioners** Howard Anderson, Sharon Yull, 2012-05-16 Full coverage of all 6 core units for the new BTEC National specifications from Edexcel. BTEC Nationals IT Practitioners is a new course text written specifically to cover the compulsory core units of the new BTEC Nationals specifications which are replacing the current BTEC National Computing scheme in 2002. Students following all National Certificate and Diploma programmes within the new scheme will find this book essential reading as it covers the core material they will be following through the duration of their course. Knowledge check questions and activities are included throughout, resulting in a clear and straightforward text which encourages independent study. The coverage is designed to cater for students following a General ICT Systems Support or Software Development course. Answers to numerical problems are included and further support material is available on the Newnes website. *Advance*

*Microprocessor* A.K. Gautam, Each topic is well explained by illustration and photographs. The book covers basic microprocessors to advanced processors in a consistent progression from theoretical concept to design considerations. The operation of various microprocessors is described with the help of pin diagram, functional diagram, and timing diagrams. A large number of working programs, problems, and the end of each chapter are summarized in the end. PIC Microcontrollers

Martin P. Bates, 2004-06-09 The use of microcontroller based solutions to everyday design problems in electronics is the most important development in the field since the introduction of the microprocessor itself. The PIC family is established as the number one microcontroller at an introductory level. Assuming no prior knowledge of microprocessors, Martin Bates provides a comprehensive introduction to microprocessor systems and applications covering all the basic principles of

microelectronics Using the latest Windows development software MPLAB the author goes on to introduce microelectronic systems through the most popular PIC devices currently used for project work both in schools and colleges as well as undergraduate university courses Students of introductory level microelectronics including microprocessor microcontroller systems courses introductory embedded systems design and control electronics will find this highly illustrated text covers all their requirements for working with the PIC Part A covers the essential principles concentrating on a systems approach The PIC itself is covered in Part B step by step leading to demonstration programmes using labels subroutines timer and interrupts Part C then shows how applications may be developed using the latest Windows software and some hardware prototyping methods The new edition is suitable for a range of students and PIC enthusiasts from beginner to first and second year undergraduate level In the UK the book is of specific relevance to AVCE as well as BTEC National and Higher National programmes in electronic engineering A comprehensive introductory text in microelectronic systems written round the leading chip for project work Uses the latest Windows development software MPLAB and the most popular types of PIC for accessible and low cost practical work Focuses on the 16F84 as the starting point for introducing the basic architecture of the PIC but also covers newer chips in the 16F8X range and 8 pin mini PICs *PC Mag*, 1988-11-15 PCMag com is a leading authority on technology delivering Labs based independent reviews of the latest products and services Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology [A Glossary of Computing Terms](#) British Computer Society, 1998 This authoritative glossary provides explanations for over 2 400 computing terms covering all aspects of computing from programming languages to personnel from architecture to applications from storage to systems software **Programming with Turbo C** S. Scott Zimmerman, Beverly B. Zimmerman, 1989 **The 80386, 80486, and Pentium Processors** Walter A. Triebel, 1998 This book is the first to concentrate on all 32 bit microprocessors and the pentium This comprehensive exploration of microprocessor technology introduces core concepts techniques and applications using the 80386 80486 and Pentium processors putting equal emphasis on assembly language software programming and microcomputer hardware interfacing The second part of this book presents software memory circuits I/O and peripherals The third part consists of PC AT business interfacing testing troubleshooting and the pentium For anyone interested in Microprocessor Technology **Advanced Microprocessor & Microcontrollers** S. K. Venkata Ram, 2004 [Using Assembly Language](#) Allen L. Wyatt, Sr., Allen Wyatt, 1992 The revised edition to cover the most recent releases of both Microsoft's Macros Assembler and Borland's Turbo Assembler Written from a programmer's perspective this power packed text explains how to use the most popular assemblers linkers and debuggers Includes a comprehensive reference section *PC Mag*, 1988-08 PCMag com is a leading authority on technology delivering Labs based independent reviews of the latest products and services Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology *C Programming on the IBM PC* S. Scott Zimmerman, 1991

*Assembly Language Programming for the Intel 80XXX Family* William B. Giles, 1991 Considers assembly programming language for the entire 80XXX family and deals with such topics as how addresses are computed what the linker and loader do and why the 80386 is a significant advance It includes end of section exercises program diagrams and examples of working programs

**Assembly Language for Intel-based Computers** Kip R. Irvine, 2007 This widely used fully updated assembly language book provides basic information for the beginning programmer interested in computer architecture operating systems hardware manipulation and compiler writing Uses the Intel IA 32 processor family as its base showing how to program for Windows and DOS Is written in a clear and straightforward manner for high readability Includes a companion CD ROM with all sample programs and Microsoftreg Macro Assembler Version 8 along with an extensive companion Website maintained by the author Covers machine architecture processor architecture assembly language fundamentals data transfer addressing and arithmetic procedures conditional processing integer arithmetic strings and arrays structures and macros 32 bit Windows programming language interface disk fundamentals BIOS level programming MS DOS programming floating point programming and IA 32 instruction encoding For embedded systems programmers and engineers communication specialists game programmers and graphics programmers

**Microprocessors** Gene A. Streitmatter, Vito Fiore, 1979

*The Waite Group's Microsoft Macro Assembler Bible* Nabajyoti Barkakati, Randall Hyde, 1992 This book explores all of the new features including improved data types support enhanced macro capabilities single pass operation and a low level optimizer Also any programmer using BASIC C FORTRAN will now be able to move their programs easily into the DOS environment with the excellent tutorial and reference material

The 80286 Microprocessor Walter A. Triebel, Avtar Singh, 1990 For an advanced course in 16 Bit micros Intel chip Incorporates hardware software and interfacing techniques geared to the 80286 Optional lab assignments are presented at the end of each chapter Includes step by step examples and practice problems

**Computer Organization and Assembly Language Programming for IBM PCs and Compatibles** Michael Thorne, 1991 This comprehensive book provides an up to date guide to programming the Intel 8086 family of microprocessors emphasizing the close relationship between microprocessor architecture and the implementation of high level languages

Robots 12 and Vision '88 Conference , 1988

Electronic Design , 1983

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