

8086 INSTRUCTION SET

OPCODE	DESCRIPTION	OPCODE	DESCRIPTION	OPCODE	DESCRIPTION
AAA	ASCII adjust addition	JNAE	label Jump if not above or equal	POPF	Push flags onto stack
AAD	ASCII adjust division	JNB	label Jump if not below	ROL	dr,es:bx Rotate left through carry
AMM	ASCII adjust multiply	JNBE	label Jump if below or equal	ROR	dr,es:bx Rotate right through carry
ASM	ASCII adjust subtraction	JNC	label Jump if no carry	REP	Repeat string operation
ADC	dr,es:bx Add with carry	JNE	label Jump if not equal	REPE	Repeat while equal
ADD	dr,es:bx Add	JNG	label Jump if not greater	REPZ	Repeat while zero
AND	dr,es:bx Logical AND	JNGE	label Jump if not greater or equal	REPNE	Repeat while not zero
CALL	proc Call a procedure	JNL	label Jump if not less	REPNZ	Repeat while not zero
CBW	Convert byte to word	JNLE	label Jump if not less or equal	RET	(pop) Return from procedure
CLC	Clear carry flag	JNO	label Jump if not overflow	ROL	dr,es:bx Rotate left
CLD	Clear direction flag	JNP	label Jump if not parity	ROR	dr,es:bx Rotate right
CLI	Clear interrupt flag	JNS	label Jump if not sign	STC	Store All into flags
CMC	Complement carry flag	JPO	label Jump if overflow	SHL	dr,es:bx Shift arithmetic left
CMF	dr,es:bx Compare	JPE	label Jump if parity odd	SHL	dr,es:bx Shift logical left
CMPS	[dr,es:bx] Compare string	JPS	label Jump if parity even	SHR	dr,es:bx Shift arithmetic right
CMPSB	- bytes	JSC	label Jump if sign	SUB	dr,es:bx Subtract with borrow
CMPSW	- words	JZ	label Jump if zero	SCAS	(dr) Scan string
CWD	Convert word to double word	LAHF	Load AH from flags	SCASB	- byte
DAA	Decimal adjust addition	LDS	dr,es:bx Load pointer using DS	SCASW	- word
DAS	Decimal adjust subtraction	LEA	dr,es:bx Load effective address	SHR	dr,es:bx Shift logical right
DEC	dt Decrement	LGS	dr,es:bx Load pointer using GS	STC	Set carry flag
DIV	es Unsigned divide	LOCK	Lock bus	STO	Set direction flag
ESC	code,es Escape	LODS [es]	Load string	STI	Set interrupt flag
HLT	Halt	LODSB	- bytes	STOS	(dt) Store string
IDIV	es Integer divide	LODSW	- words	STOIM	- byte
IMUL	es Integer multiply	LOOP	label Loop	STOIM	- word
IN	es,port Input from port	LOOPB	label Loop if equal	SUB	dr,es:bx Subtraction
INC	dt Increment	LOOPD	label Loop if zero	TEST	dr,es:bx Test (logical AND)
INT	Type Interrupt	LOOPNE	label Loop if not equal	WAIT	Wait for interrupt
INTO	Interrupt if overflow	LOOPNZ	label Loop if not zero	XCHG	dr,es:bx Exchange
IRET	Return from interrupt	MOV	dr,es:bx Move	XLAT	table Translate
JA	label Jump if above	MOV	(dr,es) Move string	XLATB	-
JAE	label Jump if above or equal	MOV	- bytes	XOR	dr,es:bx Logical exclusive OR
JB	label Jump if below	MOV	- words		
JBE	label Jump if below or equal	MUL	es Unsigned multiply		
JC	label Jump if carry	MOD	dr Negate		
JCXZ	label Jump if CX is zero	MOP	No operation		
JE	label Jump if equal	NOT	dr Logical NOT		
JG	label Jump if greater	OR	dr,es Logical OR		
JGE	label Jump if greater or equal	OUT	port,es:dx output to port		
JL	label Jump if less	POP	dr Pop word off stack		
JLE	label Jump if less or equal	POPF	dr Pop flags off stack		
JNP	label Jump	POPE	es Push word onto stack		
JNA	label Jump if not above				

Notes:

dt - destination

es - source

label - may be near or far address

label - base address

8086 Instruction Set Summary

S. K. Venkata Ram

8086 Instruction Set Summary:

Hardware and Computer Organization Arnold S. Berger, 2005-06-08 Hardware and Computer Organization is a practical introduction to the architecture of modern microprocessors. This book from the bestselling author explains how PCs work and how to make them work for you. It is designed to take students under the hood of a PC and provide them with an understanding of the complex machine that has become such a pervasive part of everyday life. It clearly explains how hardware and software cooperatively interact to accomplish real world tasks. Unlike other textbooks on this topic, Dr. Berger's book takes the software developer's point of view. Instead of simply demonstrating how to design a computer's hardware, it provides an understanding of the total machine highlighting strengths and weaknesses, explaining how to deal with memory and how to write efficient assembly code that interacts directly with and takes best advantage of the underlying hardware. The book is divided into three major sections. Part 1 covers hardware and computer fundamentals including logical gates and simple digital design. Elements of hardware development such as instruction set architecture, memory and I/O organization, and analog to digital conversion are examined in detail within the context of modern operating systems. Part 2 discusses the software at the lowest level assembly language while Part 3 introduces the reader to modern computer architectures and reflects on future trends in reconfigurable hardware. This book is an ideal reference for ECE software engineering students as well as embedded systems designers, professional engineers needing to understand the fundamentals of computer hardware and hobbyists. The renowned author's many years in industry provide an excellent basis for the inclusion of extensive real world references and insights. Several modern processor architectures are covered with examples taken from each including Intel, Motorola, MIPS and ARM.

PC Based Instrumentation and Control Mike Tooley, 2013-06-17 PC Based Instrumentation and Control is a guide to implementing computer control instrumentation and data acquisition using a standard PC and some of the more traditional computer languages. Numerous examples of configurations and working circuits as well as representative software make this a practical hands on guide to implementing PC based testing and calibration systems and increasing efficiency without compromising quality or reliability. Guidance is given on modifying the circuits and software routines to meet the reader's specific needs. The third edition includes updated coverage of PC hardware and bus systems, a new chapter on virtual instruments and an introduction to programming and software development in a modern 32 bit environment. Additional examples have been included with source code and executables available for download from the companion website www.key2control.com.

Advanced Microprocessor & Microcontrollers S. K. Venkata Ram, 2004 **PC Based Instrumentation and Control** Michael H. Tooley, 2005 PC Based Instrumentation and Control is a guide to implementing computer control instrumentation and data acquisition using a standard PC and some of the most popular computer languages. Numerous sample applications complete with examples of working circuits and representative software make this a practical hands on guide to implementing a vast range of PC based

testing measurement and control systems Advice is given on modifying the circuits and software routines to meet the reader's specific needs The third edition includes updated coverage of PC hardware and bus systems an expanded chapter on reliability and fault finding a new chapter on virtual instruments and an introduction to programming and software development in a modern 32 bit environment Additional examples have been included with source code and executables available for download from the companion website **BOOK JACKET 8086/8088 User's Manual** Intel Corporation,1989

Microprocessor Systems James W. Stewart,1990 **Assembly Language Subroutines for the 8086** Lance A. Leventhal,S. Cordes,1989 In the early days of television everything was live even the repeats Actors and actresses had to think on their feet running from set to set often while changing costume and making cuts to their scripts at the same time In this book stars such as Dame Eileen Atkins Wendy Craig and Sir Nigel Hawthorne recall the frenetic conditions in which classics such as Dixon of Dock Green and Z Cars were made and the extraordinary hazards they had to deal with scenery collapsed actors went missing and even died *Some real microprocessors* Adam Osborne,Jerry Kane,Gerry Kane,1978

8086/8088, 80286, 80386, and 80486 Assembly Language Programming Barry B. Brey,1994 **Understanding Microprocessors** Arthur William Thompson,1995 Ideal for a newly established course in microprocessors this book covers Intel and Motorola microprocessors and the basics of interfacing microprocessors to peripheral devices ALSO AVAILABLE INSTRUCTOR SUPPLEMENTS CALL CUSTOMER SUPPORT TO ORDER Instructor's Guide ISBN 0 8273 5348 0 *An Introduction to the Intel Family of Microprocessors* James L. Antonakos,1996 Fuelled by example and application this text takes readers on an in depth hands on exploration of the hardware and software giving equal treatment to both of the Intel 8088 microprocessor After examining more than 60 different applications Antonakos guides readers through the construction and programming of their own 8088 based computer This edition expands coverage to include completely new topics while it updates treatments of existing topics in an overall effort to allow greater access to the power of the personal computer

The 8086/8088 Primer Stephen P. Morse,1982 *The Intel 32-bit Microprocessors* Barry B. Brey,1995 Coverage first concentrates on real mode assembly language programming compatible with all versions of the Intel microprocessor family and compares and contrasts advanced family member with the foundational 8086 8088 This building block presentation is effective because the Intel family units are so similar that learning advanced versions is easy once the basics are understood

The 8086/8088 Family John E. Uffenbeck,1987 This book presents the full range of Intel 80x86 microprocessors in context as a component of a comprehensive microprocessor system It provides a thorough single volume coverage of all Intel processors relative to their application in the PC and is as much an introduction to the PC itself as to Intel chips Covers all PC related technologies including memory data communications and PC bus standards The second edition of The 8086 8088 Family Design Programming and Interfacing has been revised to include the latest most up to date information and technologies This edition now covers Windows a description of the MS DOS BIOS services and function calls two completely

revised software chapters an updated chapter on memory coverage of the 16550 UART and common modern standards and a new chapter on PC architecture and the common bus systems **Development Systems Handbook** Intel Corporation,1983

The 8088 and 8086 Microprocessors Walter A. Triebel,Avtar Singh,2000 For one semester courses in Microprocessors This text provides a systems level understanding of the 80X86 microprocessor and its hardware and software Equal emphasis is given to both assembly language software and microcomputer circuit design *16-bit Microprocessor Architecture* Terry Dollhoff,1979 Provide an overview of the capabilities of these microprocessors and details on how they can be programmed

Preface **The Intel Microprocessors** Barry B. Brey,1997 This fourth edition of The Intel Microprocessors 8086 8088 80186 80286 80386 80486 Pentium and Pentium Pro Processor Architecture Programming and Interfacing is a practical book for anyone interested in all programming and interfacing aspects of this important microprocessor family **Development Tools Handbook** Intel Corporation,1987 Microcomputer development language Microcomputer software development tools In circuit emulators Network development systems Microcomputer development systems System design kits PROM programming EPLD development tools *IAPX 86, 88, 186, and 188 User's Manual* Intel Corporation,1983

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Table of Contents 8086 Instruction Set Summary

1. Understanding the eBook 8086 Instruction Set Summary
 - The Rise of Digital Reading 8086 Instruction Set Summary
 - Advantages of eBooks Over Traditional Books
2. Identifying 8086 Instruction Set Summary
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an 8086 Instruction Set Summary
 - User-Friendly Interface
4. Exploring eBook Recommendations from 8086 Instruction Set Summary
 - Personalized Recommendations
 - 8086 Instruction Set Summary User Reviews and Ratings
 - 8086 Instruction Set Summary and Bestseller Lists
5. Accessing 8086 Instruction Set Summary Free and Paid eBooks

- 8086 Instruction Set Summary Public Domain eBooks
- 8086 Instruction Set Summary eBook Subscription Services
- 8086 Instruction Set Summary Budget-Friendly Options

6. Navigating 8086 Instruction Set Summary eBook Formats

- ePUB, PDF, MOBI, and More
- 8086 Instruction Set Summary Compatibility with Devices
- 8086 Instruction Set Summary Enhanced eBook Features

7. Enhancing Your Reading Experience

- Adjustable Fonts and Text Sizes of 8086 Instruction Set Summary
- Highlighting and Note-Taking 8086 Instruction Set Summary
- Interactive Elements 8086 Instruction Set Summary

8. Staying Engaged with 8086 Instruction Set Summary

- Joining Online Reading Communities
- Participating in Virtual Book Clubs
- Following Authors and Publishers 8086 Instruction Set Summary

9. Balancing eBooks and Physical Books 8086 Instruction Set Summary

- Benefits of a Digital Library
- Creating a Diverse Reading Collection 8086 Instruction Set Summary

10. Overcoming Reading Challenges

- Dealing with Digital Eye Strain
- Minimizing Distractions
- Managing Screen Time

11. Cultivating a Reading Routine 8086 Instruction Set Summary

- Setting Reading Goals 8086 Instruction Set Summary
- Carving Out Dedicated Reading Time

12. Sourcing Reliable Information of 8086 Instruction Set Summary

- Fact-Checking eBook Content of 8086 Instruction Set Summary
- Distinguishing Credible Sources

13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development

- Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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