

ARM INSTRUCTION SET

OPCODE	DESCRIPTION	JMnemonic	label	Jump if not above or equal	FUNCTION	Push flags onto stack
AAA	ASCII adjust addition	JNB	label	Jump if not below	ROL	dt, cnt Rotate left through carry
AAD	ASCII adjust division	JNBE	label	Jump if below or equal	ROR	dt, cnt Rotate right through carry
AAM	ASCII adjust multiply	JNC	label	Jump if no carry	REP	Repeat string operation
AAS	ASCII adjust subtraction	JNE	label	Jump if not equal	REPE	Repeat while equal
ADC	dt, sc Add with carry	JNG	label	Jump if not greater	REPZ	Repeat while zero
ADD	dt, sc Add	JNGE	label	Jump if not greater or equal	REPNE	Repeat while not equal
AND	dt, sc Logical AND	JNL	label	Jump if not less	REPNI	Repeat while not zero
CALL	proc Call a procedure	JNLE	label	Jump if not less or equal	RET	[pop] Return from procedure
CMB	Convert byte to word	JNI	label	Jump if not zero	ROL	dt, cnt Rotate left
CLC	Clear carry flag	JNO	label	Jump if not overflow	ROR	dt, cnt Rotate right
CML	Clear direction flag	JNP	label	Jump if not parity	SASF	Store AS into flags
CLI	Clear interrupt flag	JNS	label	Jump if not sign	SAL	dt, cnt Shift arithmetic left
CMC	Complement carry flag	JO	label	Jump if overflow	SRL	dt, cnt Shift logical left
CMF	dt, sc Compare	JPO	label	Jump if parity odd	SAR	dt, cnt Shift arithmetic right
CMPS	[dt, sc] Compare string	JP	label	Jump if parity	SB	dt, sc Subtract with borrow
CMPSB	- " " bytes	JPE	label	Jump if parity even	SCAS	[dt] Scan string
CMPSW	- " " words	JS	label	Jump if sign	SCASB	- " " byte
CWD	Convert word to double word	JZ	label	Jump if zero	SCASW	- " " word
DAA	Decimal adjust addition	LASF		Load AS from flags	SHR	dt, cnt Shift logical right
DAS	Decimal adjust subtraction	LDS	dt, sc Load pointer using DS		STC	Set carry flag
DEC	dt Decrement	LEA	dt, sc Load effective address		STD	Set direction flag
DIV	sc Unsigned divide	LES	dt, sc Load pointer using ES		STI	Set interrupt flag
ESC	code, sc Escape	LOCK		Lock bus	STOS	[dt] Store string
HLT	Halt	LODS	[sc] Load string		STOSB	- " " byte
IDIV	sc Integer divide	LODSB	- " " bytes		STOSW	- " " word
IMUL	sc Integer multiply	LODSW	- " " words		SUB	dt, sc Subtraction
IN	sc, port Input from port	LOOP	label	Loop	TEST	dt, sc Test (logical AND)
INC	dt Increment	LOOPE	label	Loop if equal	WAIT	Wait for \$087
INT	type Interrupt	LOOPE	label	Loop if zero	XCHG	dt, sc Exchange
INTO	Interrupt if overflow	LOOPE	label	Loop if not equal	XLAT	table Translate
INUT	Return from interrupt	LOOPE	label	Loop if not zero	XLATB	- " "
JL	label Jump if above	MOV	dt, sc Move		XOR	dt, sc Logical exclusive OR
JAE	label Jump if above or equal	MOVSB	[dt, sc] Move string			
JB	label Jump if below	MOVSW	- " " bytes			
JBE	label Jump if below or equal					
JC	label Jump if carry	MUL	sc Unsigned multiply			
JCXZ	label Jump if CX is zero	MOS	dt Repete			
JE	label Jump if equal	NOF	No operation			
JG	label Jump if greater	MOT	dt Logical NOT			
JGE	label Jump if greater or equal	OR	dt, sc Logical OR			
JL	label Jump if less	OUT	port, sc Output to port			
JLE	label Jump if less or equal	POP	dt Pop word off stack			
JMP	label Jump	POPF	Pop flags off stack			
JNA	label Jump if not above	PUSH	sc Push word onto stack			

Notes:

dt - destination

sc - source

label - may be near or far address

label - near address

8086 Instruction Set Summary

S. K. Venkata Ram



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Hardware and Computer Organization Arnold S. Berger, 2005-06-08 Hardware and Computer Organization is a practical introduction to the architecture of modern microprocessors This book from the bestselling author explains how PCs work and how to make them work for you It is designed to take students under the hood of a PC and provide them with an understanding of the complex machine that has become such a pervasive part of everyday life It clearly explains how hardware and software cooperatively interact to accomplish real world tasks Unlike other textbooks on this topic Dr Berger's book takes the software developer's point of view Instead of simply demonstrating how to design a computer's hardware it provides an understanding of the total machine highlighting strengths and weaknesses explaining how to deal with memory and how to write efficient assembly code that interacts directly with and takes best advantage of the underlying hardware The book is divided into three major sections Part 1 covers hardware and computer fundamentals including logical gates and simple digital design Elements of hardware development such as instruction set architecture memory and I/O organization and analog to digital conversion are examined in detail within the context of modern operating systems Part 2 discusses the software at the lowest level assembly language while Part 3 introduces the reader to modern computer architectures and reflects on future trends in reconfigurable hardware This book is an ideal reference for ECE software engineering students as well as embedded systems designers professional engineers needing to understand the fundamentals of computer hardware and hobbyists The renowned author's many years in industry provide an excellent basis for the inclusion of extensive real world references and insights Several modern processor architectures are covered with examples taken from each including Intel Motorola MIPS and ARM

PC Based Instrumentation and Control Mike Tooley, 2013-06-17 PC Based Instrumentation and Control is a guide to implementing computer control instrumentation and data acquisition using a standard PC and some of the more traditional computer languages Numerous examples of configurations and working circuits as well as representative software make this a practical hands on guide to implementing PC based testing and calibration systems and increasing efficiency without compromising quality or reliability Guidance is given on modifying the circuits and software routines to meet the reader's specific needs The third edition includes updated coverage of PC hardware and bus systems a new chapter on virtual instruments and an introduction to programming and software development in a modern 32 bit environment Additional examples have been included with source code and executables available for download from the companion website www.key2control.com

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testing measurement and control systems Advice is given on modifying the circuits and software routines to meet the reader's specific needs The third edition includes updated coverage of PC hardware and bus systems an expanded chapter on reliability and fault finding a new chapter on virtual instruments and an introduction to programming and software development in a modern 32 bit environment Additional examples have been included with source code and executables available for download from the companion website BOOK JACKET **8086/8088 User's Manual** Intel Corporation,1989

Microprocessor Systems James W. Stewart,1990 **Assembly Language Subroutines for the 8086** Lance A. Leventhal,S. Cordes,1989 In the early days of television everything was live even the repeats Actors and actresses had to think on their feet running from set to set often while changing costume and making cuts to their scripts at the same time In this book stars such as Dame Eileen Atkins Wendy Craig and Sir Nigel Hawthorne recall the frenetic conditions in which classics such as *Dixon of Dock Green* and *Z Cars* were made and the extraordinary hazards they had to deal with scenery collapsed actors went missing and even died *Some real microprocessors* Adam Osborne,Jerry Kane,Gerry Kane,1978

8086/8088, 80286, 80386, and 80486 Assembly Language Programming Barry B. Brey,1994 **Understanding Microprocessors** Arthur William Thompson,1995 Ideal for a newly established course in microprocessors this book covers Intel and Motorola microprocessors and the basics of interfacing microprocessors to peripheral devices ALSO AVAILABLE INSTRUCTOR SUPPLEMENTS CALL CUSTOMER SUPPORT TO ORDER Instructor's Guide ISBN 0 8273 5348 0 *An Introduction to the Intel Family of Microprocessors* James L. Antonakos,1996 Fuelled by example and application this text takes readers on an in depth hands on exploration of the hardware and software giving equal treatment to both of the Intel 8088 microprocessor After examining more than 60 different applications Antonakos guides readers through the construction and programming of their own 8088 based computer This edition expands coverage to include completely new topics while it updates treatments of existing topics in an overall effort to allow greater access to the power of the personal computer

The 8086/8088 Primer Stephen P. Morse,1982 *The Intel 32-bit Microprocessors* Barry B. Brey,1995 Coverage first concentrates on real mode assembly language programming compatible with all versions of the Intel microprocessor family and compares and contrasts advanced family member with the foundational 8086 8088 This building block presentation is effective because the Intel family units are so similar that learning advanced versions is easy once the basics are understood

The 8086/8088 Family John E. Uffenbeck,1987 This book presents the full range of Intel 80x86 microprocessors in context as a component of a comprehensive microprocessor system It provides a thorough single volume coverage of all Intel processors relative to their application in the PC and is as much an introduction to the PC itself as to Intel chips Covers all PC related technologies including memory data communications and PC bus standards The second edition of *The 8086 8088 Family* Design Programming and Interfacing has been revised to include the latest most up to date information and technologies This edition now covers Windows a description of the MS DOS BIOS services and function calls two completely

revised software chapters an updated chapter on memory coverage of the 16550 UART and common modern standards and a new chapter on PC architecture and the common bus systems **Development Systems Handbook** Intel Corporation,1983

The 8088 and 8086 Microprocessors Walter A. Triebel,Avtar Singh,2000 For one semester courses in Microprocessors This text provides a systems level understanding of the 80X86 microprocessor and its hardware and software Equal emphasis is given to both assembly language software and microcomputer circuit design *16-bit Microprocessor Architecture* Terry Dollhoff,1979 Provide an overview of the capabilities of these microprocessors and details on how they can be programmed Preface **The Intel Microprocessors** Barry B. Brey,1997 This fourth edition of The Intel Microprocessors 8086 8088 80186 80286 80386 80486 Pentium and Pentium Pro Processor Architecture Programming and Interfacing is a practical book for anyone interested in all programming and interfacing aspects of this important microprocessor family **Development Tools Handbook** Intel Corporation,1987 Microcomputer development language Microcomputer software development tools In circuit emulators Network development systems Microcomputer development systems System design kits PROM programming EPLD development tools *IAPX 86, 88, 186, and 188 User's Manual* Intel Corporation,1983

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