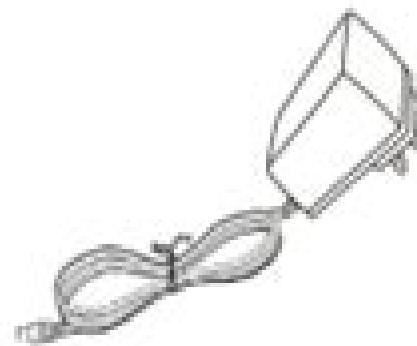


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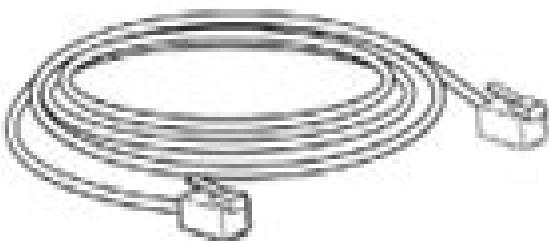
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C.



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Ata Configuration Guide

Michael Meyers, Scott Jernigan

Ata Configuration Guide:

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Forensic Computing Anthony Sammes, Brian Jenkinson, 2007-08-18 In the second edition of this very successful book Tony Sammes and Brian Jenkinson show how information held in computer systems can be recovered and how it may be deliberately hidden or subverted for criminal purposes Forensic Computing A Practitioner's Guide is illustrated by plenty of case studies and worked examples and will help practitioners and students gain a clear understanding of how to recover information from computer systems in such a way as to ensure that its integrity cannot be challenged and that it will be accepted as admissible evidence in court the principles involved in password protection and data encryption the evaluation procedures used in circumventing these safeguards the particular legal issues associated with computer generated evidence and how to ensure admissibility of such

evidence This edition is fully expanded and updated with treatment of metadata files NFTS systems CHS and LBA addressing and alternate data streams *High-speed Serial Buses in Embedded Systems* Feng Zhang,2020-01-03 This book describes the most frequently used high speed serial buses in embedded systems especially those used by FPGAs These buses employ SerDes JESD204 SRIO PCIE Aurora and SATA protocols for chip to chip and board to board communication and CPCIE VPX FC and Infiniband protocols for inter chassis communication For each type the book provides the bus history and version info while also assessing its advantages and limitations Furthermore it offers a detailed guide to implementing these buses in FPGA design from the physical layer and link synchronization to the frame format and application command Given its scope the book offers a valuable resource for researchers R D engineers and graduate students in computer science or electronics who wish to learn the protocol principles structures and applications of high speed serial buses **PC Mag** ,1999-03-23

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