

8085 INSTRUCTION SET

INSTRUCTION DETAILS

DATA TRANSFER INSTRUCTIONS

Opcode	Operand	Description
Copy from source to destination		
MOV	Rd, Rs M, Rs Rd, M	This instruction copies the contents of the source register into the destination register; the contents of the source register are not altered. If one of the operands is a memory location, its location is specified by the contents of the HL registers. Example: MOV B, C or MOV B, M
Move immediate 8-bit		
MVI	Rd, data M, data	The 8-bit data is stored in the destination register or memory. If the operand is a memory location, its location is specified by the contents of the HL registers. Example: MVI B, 57H or MVI M, 57H
Load accumulator		
LDA	16-bit address	The contents of a memory location, specified by a 16-bit address in the operand, are copied to the accumulator. The contents of the source are not altered. Example: LDA 2034H
Load accumulator indirect		
LDAX	B/D Reg. pair	The contents of the designated register pair point to a memory location. This instruction copies the contents of that memory location into the accumulator. The contents of either the register pair or the memory location are not altered. Example: LDAX B
Load register pair immediate		
LXI	Reg. pair, 16-bit data	The instruction loads 16-bit data in the register pair designated in the operand. Example: LXI H, 2034H or LXI H, XYZ
Load H and L registers direct		
LHLD	16-bit address	The instruction copies the contents of the memory location pointed out by the 16-bit address into register L and copies the contents of the next memory location into register H. The contents of source memory locations are not altered. Example: LHLD 2040H

8085 Microprocessor Instruction Set

Barry B. Brey



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Microprocessor Instruction Sets and Software Principles David L. Heiserman, 1983 **Microprocessors and Microcomputer-Based System Design** Mohamed Rafiquzzaman, 1995-05-25 Microprocessors and Microcomputer Based System Design Second Edition builds on the concepts of the first edition It discusses the basics of microprocessors various 32 bit microprocessors the 8085 microprocessor the fundamentals of peripheral interfacing and Intel and Motorola microprocessors This edition includes new topics such as floating point arithmetic Program Array Logic and flash memories It covers the popular Intel 80486 80960 and Motorola 68040 as well as the Pentium and PowerPC microprocessors The final chapter presents system design concepts applying the design principles covered in previous chapters to sample problems

Microprocessors and Microcontrollers Atul P. Godse, Dr. Deepali A. Godse, 2020-12-01 The book is written for an undergraduate course on the 8085 microprocessor and 8051 microcontroller It provides comprehensive coverage of the hardware and software aspects of 8085 microprocessor and 8051 microcontroller The book is divided into two parts The first part focuses on 8085 microprocessor It teaches you the 8085 architecture instruction set Assembly Language Programming ALP interfacing 8085 with support chips memory and peripheral ICs 8251 8253 8255 8259 8237 and 8279 It also explains the interfacing of 8085 with data converters ADC and DAC and introduces a temperature control system and data acquisition system design The second part focuses on 8051 microcontroller It teaches you the 8051 architecture instruction set programming 8051 with ALP and C and interfacing 8051 with external memory It also explains timers counters serial port and interrupts of 8051 and their programming in ALP and C It also covers the interfacing 8051 with data converters ADC and DAC keyboards LCDs LEDs stepper motors servo motors and introduces the washing machine control system design

Microprocessors [GATE, PSUS AND ES Examination] Satish K Karna, Test Prep for Microprocessors GATE PSUS AND ES Examination *CUET PG Entrance Exam Data Science 25 Sample Paper with Explanation for 2026 | Includes Previous Years (2022-2025) Exam Papers* Nodia, 2025-12-22 The NODIA Data Science Sample Papers Book is designed to support exam preparation through structured exam focused practice aligned strictly with current NTA CUET PG standards Fully updated as per the latest NTA CUET PG Data Science syllabus the book includes solved previous year question papers 2022 2025 to help learners understand exam trends and difficulty levels It also features 25 expert curated mock tests for real exam simulation and self assessment along with concise 50 word explanations for every answer to support quick revision and effective concept recall 2025-26 RRB JE Stage-II Electronics & Allied Engineering Solved Papers and Practice Book YCT Expert Team , 2025 26 RRB JE Stage II Electronics Allied Engineering Solved Papers and Practice Book 576 1095 E This book contains 128 sets of the previous year solved papers with 5042 objective questions **Microprocessors** Mohamed Rafiquzzaman, 1992 *Some real microprocessors* Adam Osborne, Jerry Kane, Gerry Kane, 1978 **Schaum's Outline of Theory and Problems of Microprocessor Fundamentals** Roger L. Tokheim, 1983 The Microprocessor Handbook

Elmer Poe, 1983 Common characteristics of microprocessors The 8080 The 8085 The Z 80 The 6800 The 6802 The 6809 The 6502 A brief introduction to 16 bit processors The 8086 and 8088 The Z8000 The 68000 Parallel I/O chips Serial I/O chips Common memory chips Microprocessors and Peripherals Barry B. Brey, 1988 **Digital Electronics with Microprocessor Applications** Alan C. Dixon, James L. Antonakos, 1987 A textbook for courses in digital electronics and microprocessors offered in departments of electrical engineering technology or computer science The book covers the basics of digital logic design and the design of microprocessor based systems Also covered are computer fundamentals and microprocessor hardware and software 8085 with many programming examples The text describes most important available microprocessors with laboratory exercises instructional objectives and self evaluation questions Computer Systems Organization & Architecture John D. Carpinelli, 2001 This book provides up to date coverage of fundamental concepts for the design of computers and their subsystems It presents material with a serious but easy to understand writing style that makes it accessible to readers without sacrificing important topics The book emphasizes a finite state machine approach to CPU design which provides a strong background for reader understanding It forms a solid basis for readers to draw upon as they study this material and in later engineering and computer science practice The book also examines the design of computer systems including such topics as memory hierarchies input output processing interrupts and direct memory access as well as advanced architectural aspects of parallel processing To make the material accessible to beginners the author has included two running examples of increasing complexity the Very Simple CPU which contains four instruction sets and shows very simple CPU design and the Relatively Simple CPU which contains 16 instruction sets and adds enough complexity to illustrate more advanced concepts Each chapter features a real world machine on which the discussed organization and architecture concepts are implemented This book is designed to teach computer organization architecture to engineers and computer scientists **Microprocessor Architecture, Programming, and Applications with the 8085** Ramesh S. Gaonkar, 1996 Revised to include the most recent technological changes this comprehensive survey offers an integrated treatment of both the hardware and software aspects of the microprocessor focusing on the 8085 microprocessor family to teach the basic concepts underlying programmable devices Providing a sound pedagogy from basic concepts to applications it prepares users to apply concepts learned to a variety of situations they may encounter in their future jobs *The Intel Microprocessors* Barry B. Brey, 1997 This fourth edition of *The Intel Microprocessors* 8086 8088 80186 80286 80386 80486 Pentium and Pentium Pro Processor Architecture Programming and Interfacing is a practical book for anyone interested in all programming and interfacing aspects of this important microprocessor family **The Z80 Microprocessor** Ramesh S. Gaonkar, 2001 This text is intended for microprocessor courses at the undergraduate level in technology engineering and computer science Now in its third edition it provides a comprehensive treatment of the microprocessor covering both hardware and software based on the Z80 microprocessor family This edition preserves the focus of the earlier editions and

includes the following changes Chapters have been revised to include the most recent technological changes in 32 and 64 bit microprocessors and 8 bit microcontrollers Several illustrative programs have been added throughout the text Complete data sheets for the LM 135 temperature sensor and LCD panel and a complete list of Z80 instructions with machine cycles T states and flags are included in the Appendixes Appendix G which contains answers to selected questions has been added

Osborne 4 & 8-bit Microprocessor Handbook Adam Osborne, Gerry Kane, 1981 **The Illustrated Computer**

Dictionary and Handbook Les Cowan, 1983-12 Defines Hi tech Terms in Plain English Supported by Diagrams for Home or School Use Beginner's Guide to Microprocessors Charles Minot Gilmore, 1984 Gives Details on BASIC Logic

Demonstrates the Microprocessor s Function as Integrated Circuit Also Covers I O Devices Systems Software *CP/M Assembly Language Programming* Ken Barbier, 1983

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