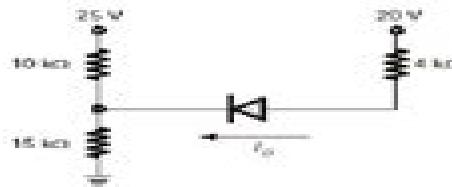


Q1.1

If the diode in the circuit shown below is ideal, the current I_D will be

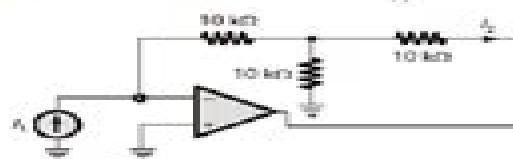


- a. 0 mA
- b. 0.5 mA
- c. 1 mA
- d. 2 mA

Attempt | Connect | Connect Ans. | In

Q1.2

For the circuit shown below the value of $\frac{A_2}{A_1}$ is



- a. -1
- b. -2
- c. -1
- d. -2

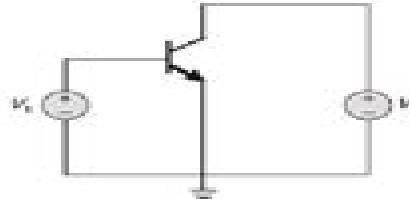
Q1.3

Consider two amplifiers A_1 and A_2 . The rise time for first stage is 0.3 msec and for the second stage is 0.4 msec. If the two systems are connected in cascade form with each other then the rise time of the combined system will be

- a. 0.5 msec
- b. 0.55 msec
- c. 0.4 msec
- d. 0.3 msec

Q1.4

The transistor in the circuit shown below is biased to operate in



- a. Active Region for $V_1 > V_2$; $V_1, V_2 > 0$
- b. Active Region for $V_1 < V_2$; $V_1, V_2 > 0$
- c. Saturation Region for $V_1 > V_2$; $V_1, V_2 > 0$
- d. Saturation Region for $V_1 < V_2$; $V_1, V_2 > 0$

Analog Electronics Solved Problems

Rudolf F. Graf

Analog Electronics Solved Problems:

Lecture Notes in Analog Electronics Vančo B. Litovski, 2025-01-17 Prof Vančo Litovski was born in 1947 in Rakita, South Macedonia, Greece. He graduated from the Faculty of Electronic Engineering in Niš in 1970 and obtained his M.Sc. in 1974 and his Ph.D. in 1977. He was appointed as a teaching assistant at the Faculty of Electronic Engineering in 1970 and became a full professor at the same faculty in 1987. He was elected as a visiting professor honoris causa at the University of Southampton in 1999. From 1987 until 1990 he was a consultant to the CEO of Ei and was the head of the Chair of Electronics at the Faculty of Electronic Engineering in Niš for 12 years. From 2015 to 2017 he was a researcher at the University of Bath. He received several awards including from the Faculty of Electronic Engineering Charter in 1980, Charter in 1985, and a Special Recognition in 1995, and the University of Niš Plaque 1985.

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