

Intel 8085 Instructions

1. Data Transfer Group

1. MOV r1, r2 (Move Data; Move the content of the one register to another). $[r1] \leftarrow [r2]$
2. MOV r, m (Move the content of memory register). $r \leftarrow [M]$
3. MOV M, r. (Move the content of register to memory). $M \leftarrow [r]$
4. MVI r, data. (Move immediate data to register). $[r] \leftarrow \text{data}$.
5. MVI M, data. (Move immediate data to memory). $M \leftarrow \text{data}$.
6. LXI rp, data 16. (Load register pair immediate). $[rp] \leftarrow \text{data 16 bits}$, $[rh] \leftarrow 8 \text{ LSBs of data}$.
7. LDA addr. (Load Accumulator direct). $[A] \leftarrow [\text{addr}]$.
8. STA addr. (Store accumulator direct). $[\text{addr}] \leftarrow [A]$.
9. LHLD addr. (Load H-L pair direct). $[L] \leftarrow [\text{addr}]$, $[H] \leftarrow [\text{addr}+1]$.
10. SHLD addr. (Store H-L pair direct) $[\text{addr}] \leftarrow [L]$, $[\text{addr}+1] \leftarrow [H]$.
11. LDAX rp. (LOAD accumulator indirect) $[A] \leftarrow [[rp]]$
12. STAX rp. (Store accumulator indirect) $[[rp]] \leftarrow [A]$.
13. XCHG. (Exchange the contents of H-L with D-E pair) $[H-L] \leftrightarrow [D-E]$.

2. Arithmetic Group

1. ADD r. (Add register to accumulator) $[A] \leftarrow [A] + [r]$.
2. ADD M. (Add memory to accumulator) $[A] \leftarrow [A] + [[H-L]]$.
3. ADC r. (Add register with carry to accumulator). $[A] \leftarrow [A] + [r] + [CS]$.
4. ADC M. (Add memory with carry to accumulator) $[A] \leftarrow [A] + [[H-L]] + [CS]$.
5. ADI data (Add immediate data to accumulator) $[A] \leftarrow [A] + \text{data}$.
6. ACI data (Add with carry immediate data to accumulator). $[A] \leftarrow [A] + \text{data} + [CS]$.
7. DAD rp. (Add register pair to H-L pair). $[H-L] \leftarrow [H-L] + [rp]$.
8. SUB r. (Subtract register from accumulator). $[A] \leftarrow [A] - [r]$.
9. SUB M. (Subtract memory from accumulator). $[A] \leftarrow [A] - [[H-L]]$.
10. SBB r. (Subtract register from accumulator with borrow). $[A] \leftarrow [A] - [r] - [CS]$.
11. SBB M. (Subtract memory from accumulator with borrow). $[A] \leftarrow [A] - [[H-L]] - [CS]$.
12. SUI data. (Subtract immediate data from accumulator) $[A] \leftarrow [A] - \text{data}$.
13. SBI data. (Subtract immediate data from accumulator with borrow). $[A] \leftarrow [A] - \text{data} - [CS]$.
14. INR r (Increment register content) $[r] \leftarrow [r] + 1$.
15. INR M. (Increment memory content) $[[H-L]] \leftarrow [[H-L]] + 1$.
16. DCR r. (Decrement register content). $[r] \leftarrow [r] - 1$.
17. DCR M. (Decrement memory content) $[[H-L]] \leftarrow [[H-L]] - 1$.
18. INX rp. (Increment register pair) $[rp] \leftarrow [rp] + 1$.
19. DCX rp (Decrement register pair) $[rp] \leftarrow [rp] - 1$.
20. DAA (Decimal adjust accumulator) .

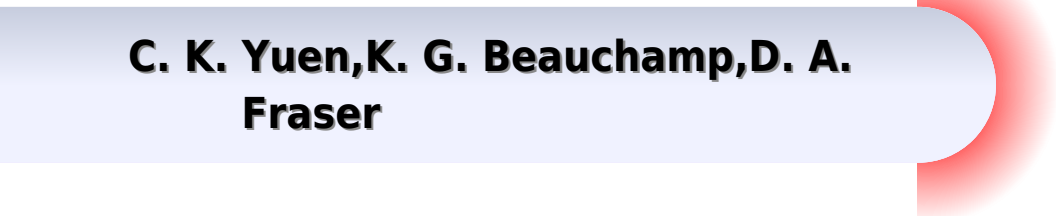
The instruction DAA is used in the program after ADD, ADI, ACI, ADC, etc instructions. After the execution of ADD, ADC, etc instructions the result is in hexadecimal and it is placed in the accumulator. The DAA instruction operates on this result and gives the final result in the decimal system. It uses carry and auxiliary carry for decimal adjustment. 6 is added to 4 LSBs of the content of the accumulator if their value lies in between A and F or the AC flag is set to 1. Similarly, 6 is also added to 4 MSBs of the content of the accumulator if their value lies in between A and F or the CS flag is set to 1. All status flags are affected. When DAA is used data should be in decimal numbers.

3. Logical Group

1. ANA r. (AND register with accumulator) $[A] \leftarrow [A] \wedge [r]$.
2. ANA M. (AND memory with accumulator). $[A] \leftarrow [A] \wedge [[H-L]]$.
3. ANI data. (AND immediate data with accumulator) $[A] \leftarrow [A] \wedge \text{data}$.
4. ORA r. (OR register with accumulator) $[A] \leftarrow [A] \vee [r]$.
5. ORA M. (OR memory with accumulator) $[A] \leftarrow [A] \vee [[H-L]]$.
6. ORI data. (OR immediate data with accumulator) $[A] \leftarrow [A] \vee \text{data}$.
7. XRA r. (EXCLUSIVE – OR register with accumulator) $[A] \leftarrow [A] \vee [r]$

8085 Microprocessor Instruction Set With Explanation

**C. K. Yuen, K. G. Beauchamp, D. A.
Fraser**



8085 Microprocessor Instruction Set With Explanation:

Fundamental of Microprocessors & its Application A.K.Chhabra, 2005 World first Microprocessor INTEL 4004 a 4 bit Microprocessor came in 1971 forming the series of first generation microprocessor Science then with more and advancement in technology there have been five Generations of Microprocessors However the 8085 an 8 bit Microprocessor is still the most popular Microprocessor The present book provided a simple explanation about the Microprocessor its programming and interfacing The book contains the description mainly of the 8 bit programmable Interrupt Interval Timer Counter 8253 Programmable communication Interface 8251 USART 8251A and INTEL 8212 8155 8256 8755 and 8279

Microprocessors & Introduction to Microcontroller Atul P. Godse, Dr. Deepali A. Godse, 2020-12-01 The book is written for an undergraduate course on the 8085 and 8086 microprocessors and 8051 microcontroller It provides comprehensive coverage of the hardware and software aspects of 8085 and 8086 microprocessors and 8051 microcontroller The book uses plain and lucid language to explain each topic A large number of programming examples is the feature of this book The book provides the logical method of describing the various complicated concepts and stepwise techniques for easy understanding making the subject more interesting The book is divided into three parts The first part focuses on the 8085 microprocessor It teaches you the 8085 architecture pin description bus organization instruction set addressing modes instruction formats Assembly Language Programming ALP instruction timing diagrams interrupts and interfacing 8085 with support chips memory and peripheral ICs 8251 8253 8255 8259 and 8279 It also explains the interfacing of 8085 with data converters ADC and DAC and introduces a temperature control system design The second part focuses on the 8086 microprocessor It teaches you the 8086 architecture register organization memory segmentation interrupts addressing modes operating modes minimum and maximum modes interfacing 8086 with support chips minimum and maximum mode 8086 systems and timings The third part focuses on the 8051 microcontroller It teaches you the 8051 architecture pin description instruction set programming 8051 and interfacing 8051 with external memory It explains timers counters serial port interrupts of 8051 and their programming It also describes the interfacing 8051 with keyboards LCDs and LEDs and explains the control of servomotor stepper motors and washing machine using 8051

Microprocessors and Microcontrollers Atul P. Godse, Dr. Deepali A. Godse, 2020-12-01 The book is written for an undergraduate course on the 8085 microprocessor and 8051 microcontroller It provides comprehensive coverage of the hardware and software aspects of 8085 microprocessor and 8051 microcontroller The book is divided into two parts The first part focuses on 8085 microprocessor It teaches you the 8085 architecture instruction set Assembly Language Programming ALP interfacing 8085 with support chips memory and peripheral ICs 8251 8253 8255 8259 8237 and 8279 It also explains the interfacing of 8085 with data converters ADC and DAC and introduces a temperature control system and data acquisition system design The second part focuses on 8051 microcontroller It teaches you the 8051 architecture instruction set programming 8051 with

ALP and C and interfacing 8051 with external memory It also explains timers counters serial port and interrupts of 8051 and their programming in ALP and C It also covers the interfacing 8051 with data converters ADC and DAC keyboards LCDs LEDs stepper motors servo motors and introduces the washing machine control system design **Fundamental of Digital Electronics And Microprocessors** A.K.Chhabra,2005 In the recent years there has been rapid advances in the field of Digital Electronics and Microprocessor This book is intended to help students to keep pace with these latest developments The Present book is revised version of earlier book Introduction to Digital Computers by the same author Now this book is written in a lucid and simple language which gives clear explanation of basics of Digital Electronics Computers and microprocessors A Textbook of Digital Electronics RS Sedha,2008 While writing this treatise I have constantly kept in mind the requirements of all the students regarding the latest as well as changing trend of their examinations To make it really useful for the students latest examination questions of various indian universities as well as other examinations bodies have been included The Book has been written in easy style with full details and illustrations **Microprocessors and Microcomputer-Based System Design** Mohamed Rafiquzzaman,1995-05-25 Microprocessors and Microcomputer Based System Design Second Edition builds on the concepts of the first edition It discusses the basics of microprocessors various 32 bit microprocessors the 8085 microprocessor the fundamentals of peripheral interfacing and Intel and Motorola microprocessors This edition includes new topics such as floating point arithmetic Program Array Logic and flash memories It covers the popular Intel 80486 80960 and Motorola 68040 as well as the Pentium and PowerPC microprocessors The final chapter presents system design concepts applying the design principles covered in previous chapters to sample problems

Schaum's Outline of Theory and Problems of Microprocessor Fundamentals Roger L. Tokheim,1983

Microprocessor Software Gene A. Streitmatter,1981

An Introduction to the Intel Family of Microprocessors

James L. Antonakos,1996 Fuelled by example and application this text takes readers on an in depth hands on exploration of the hardware and software giving equal treatment to both of the Intel 8088 microprocessor After examining more than 60 different applications Antonakos guides readers through the construction and programming of their own 8088 based computer This edition expands coverage to include completely new topics while it updates treatments of existing topics in an overall effort to allow greater access to the power of the personal computer Microprocessor Systems in Signal Processing

C. K. Yuen,K. G. Beauchamp,D. A. Fraser,1989 Very Good No Highlights or Markup all pages are intact **Introduction to the 8085A Microcomputer** Adi J. Khambata,1987 Provided here is specific information on the 8085A family hardware and software Using a unique approach it covers the three most popular and widely used 8 bit microcomputer products ZILOG Z80 INTEL 8085A presented in three separate softcover supplements The book was originally intended as a supplement to Khambata s textbook Microprocessors Microcomputers Architecture Software and Systems 2nd Edition but it may also be used as a supplement to other basic texts or as a brief stand alone introduction to the 8085A allowing for much flexibility in

teaching Each chapter includes a list of objectives and end of chapter questions **Microprocessor Systems** James W. Stewart,1990 *Microprocessor Programming and Software Development* Fraser George Duncan,1979 *Computer Systems Organization & Architecture* John D. Carpinelli,2001 This book provides up to date coverage of fundamental concepts for the design of computers and their subsystems It presents material with a serious but easy to understand writing style that makes it accessible to readers without sacrificing important topics The book emphasizes a finite state machine approach to CPU design which provides a strong background for reader understanding It forms a solid basis for readers to draw upon as they study this material and in later engineering and computer science practice The book also examines the design of computer systems including such topics as memory hierarchies input output processing interrupts and direct memory access as well as advanced architectural aspects of parallel processing To make the material accessible to beginners the author has included two running examples of increasing complexity the Very Simple CPU which contains four instruction sets and shows very simple CPU design and the Relatively Simple CPU which contains 16 instruction sets and adds enough complexity to illustrate more advanced concepts Each chapter features a real world machine on which the discussed organization and architecture concepts are implemented This book is designed to teach computer organization architecture to engineers and computer scientists **The Illustrated Computer Dictionary and Handbook** Les Cowan,1983-12 Defines Hi tech Terms in Plain English Supported by Diagrams for Home or School Use *Digital Electronics with Microprocessor Applications* Alan C. Dixon,James L. Antonakos,1987 A textbook for courses in digital electronics and microprocessors offered in departments of electrical engineering technology or computer science The book covers the basics of digital logic design and the design of microprocessor based systems Also covered are computer fundamentals and microprocessor hardware and software 8085 with many programming examples The text describes most important available microprocessors with laboratory exercises instructional objectives and self evaluation questions *Microprocessor Architecture, Programming, and Applications with the 8085* Ramesh S. Gaonkar,1996 Revised to include the most recent technological changes this comprehensive survey offers an integrated treatment of both the hardware and software aspects of the microprocessor focusing on the 8085 microprocessor family to teach the basic concepts underlying programmable devices Providing a sound pedagogy from basic concepts to applications it prepares users to apply concepts learned to a variety of situations they may encounter in their future jobs **System Design with Microprocessors** D. Zissos,J. C. Bathory,1978 Logic design The microprocessor chip Microprocessor system and software Microprocessor interfaces Test and skip systems Interrupt systems DMA systems Distributed systems Intel 8080 and 8085 Motorola 6800 MCS 6502 *Microprocessors* Victor E. Gibson,1994 **Design of Microprocessor Based Systems** Vinod Kumar Bansal,1985

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