The 8088 Instruction Set (3)

Mnemonic	Description	Operands	Status flags O	S	Z	C
TEST(B)	Test operands	e ↔ r, e ↔ #	0	•	•	0
CMP(B)	Compare operands	e ↔ r, e ↔ #	•	•	•	•
STD	Set direction flag (↓)	•	•	•	•	•
CLD	Clear direction flag (↑)	•	•	•	•	•
STC	Set carry flag	•	•	•	•	1
CLC	Clear carry flag	•	•	•	•	0
CMC	Complement carry	•	•	•	•	•
LOOP	Jump back if decremented CX ≥ 0	label	•	•	•	•
LOOPZ LOOPE	Back if Z=1 and DEC(CX)≥0	label	•	•	•	•
LOOPNZ LOOPNE	Back if Z=0 and DEC(CX)≥0	label	•	•	•	•
REP REPZ REPNZ	Repeat string instruction	string instruction	•	•	•	•
MOVS(B)	Move word string	•	•	•	•	•
LODS(B)	Load word string	•	•	•	•	•
STOS(B)	Store word string	•	•	•	•	•
SCAS(B)	Scan word string	•	•	•	•	•
CMPS(B)	Compare word string	•	•	•	•	•
JCC	Jump according conditions	label	•	•	•	•
JMP	Jump to label	e, label	•	•	•	•
CALL	Jump to subroutine	e, label	•	•	•	•
RET	Return from subroutine	-; #	•	•	•	•
SYS	System call trap	•	•	•	•	•

Some of the most important 8088 instructions.

8088 Instruction Set Datasheet

Adam Osborne,Gerry Kane

8088 Instruction Set Datasheet:

8086/8088 User's Manual Intel Corporation,1989 *Osborne 16-bit Microprocessor Handbook* Adam Osborne,Gerry Kane,1981 The national semiconductor PACE and INS8900 The general instrument CP 1600 The Texas instruments TMS 9900 TMS 9980 and TMS 9440 products Single chip nova microcomputer central processing units The intel 8086 The zilog Z8000 series **Program Interfacing 8086 8088** Goody,1992 **Hardware and Computer Organization** Arnold S. Berger,2005-05-06 Hardware and Computer Organization is a practical introduction to the architecture of modern microprocessors for students and professionals alike It is designed to take readers under the hood of modern embedded computer systems and PCs and provide them with an understanding of these complex machines that has become such a pervasive part of everyday life Unlike other texts on this topic Dr Berger s book takes the software developer s point of view Instead of simply demonstrating how to design a computer s hardware it provides an understanding of the total machine highlighting strengths and weaknesses explaining how to deal with memory and how to write efficient assembly code that interacts directly with and takes best advantage of the underlying machine BOOK JACKET *Processor Description Languages* Prabhat Mishra,Nikil Dutt,2011-07-28 Efficient design of embedded processors plays a critical role in embedded systems design Processor description languages and their associated specification exploration and rapid prototyping methodologies are used to find the best possible design for a given set of applications under various design constraints such as area power and performance This book is the first comprehensive survey of modern architecture description languages and will be an invaluable reference for embedded system architects designers developers and validation engineers Readers will see that the use of particular architecture description languages will lead to productivity gains in designing particular application specific types of embedded processors Comprehensive coverage of all modern architecture description languages use the right ADL to design your processor to fit your application Most up to date information available about each architecture description language from the developers save time chasing down reliable documentation Describes how each architecture desccription language enables key design automation tasks such as simulation synthesis and testing fit the ADL to your design cycle IAPX 86/88, 186/188 User's Manual Hardware Reference Intel Corporation,1985 **IAPX 86, 88 User's Manual** Intel Corporation,1981 **IAPX 86, 88, 186, and 188 User's Manual** ,1983 **EDN** ,1989 **Microcomputer Theory and Servicing** Stuart Asser,Vincent Stigliano,Richard Bahrenburg,1993 **An Introduction to the Intel Family of Microprocessors** James L. Antonakos,1993 This introduction to the Intel microprocessors offers equal treatment of hardware and software applications and a build your own 8088 based computer project The text takes students through the software interrupts DOS programming hardware memory input output and peripherals Microprocessors Kenneth Muchow,Bill R. Deem,1983 **8086/88 Assembly Language Programming** Leo J. Scanlon,1984 **Electronics Now** ,1997 Monthly Catalog of United States Government Publications ,1985

Microprocessor-based Design Michael Slater,1989   **Monthly Catalogue, United States Public Documents** ,1985-11   The 8086/8088 Family John E. Uffenbeck,1987 This book presents the full range of Intel 80x86 microprocessors in context as a component of a comprehensive microprocessor system It provides a thorough single volume coverage of all Intel processors relative to their application in the PC and is as much an introduction to the PC itself as to Intel chips Covers all PC related technologies including memory data communications and PC bus standards The second edition of The 8086 8088 Family Design Programming and Interfacing has been revised to include the latest most up to date information and technologies This edition now covers Windows a description of the MS DOS BIOS services and function calls two completely revised software chapters an updated chapter on memory coverage of the 16550 UART and common modern standards and a new chapter on PC architecture and the common bus systems   **Bowker's Complete Sourcebook of Personal Computing, 1985** R.R. Bowker Company,1984 Provides Listings of Hardware Software Peripherals Currently Available as Well as Books Magazines Clubs User Groups Virtually All Other Microcomputer related Services Includes Background Information Glossary   *Microsystem Components Handbook* Intel Corporation,1985

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