



# ARM System Developer's Guide

Designing and  
Optimizing System  
Software

Andrew H. SLOSS  
Dominic SYMES  
Chris WRIGHT

ARM Ltd

# Arm System Developer Guide

**S Nieto**



## **Arm System Developer Guide:**

**ARM System Developer's Guide** Andrew Sloss, Dominic Symes, Chris Wright, 2004-05-10 Over the last ten years the ARM architecture has become one of the most pervasive architectures in the world with more than 2 billion ARM based processors embedded in products ranging from cell phones to automotive braking systems A world wide community of ARM developers in semiconductor and product design companies includes software developers system designers and hardware engineers To date no book has directly addressed their need to develop the system and software for an ARM based system This text fills that gap This book provides a comprehensive description of the operation of the ARM core from a developer s perspective with a clear emphasis on software It demonstrates not only how to write efficient ARM software in C and assembly but also how to optimize code Example code throughout the book can be integrated into commercial products or used as templates to enable quick creation of productive software The book covers both the ARM and Thumb instruction sets covers Intel s XScale Processors outlines distinctions among the versions of the ARM architecture demonstrates how to implement DSP algorithms explains exception and interrupt handling describes the cache technologies that surround the ARM cores as well as the most efficient memory management techniques A final chapter looks forward to the future of the ARM architecture considering ARMv6 the latest change to the instruction set which has been designed to improve the DSP and media processing capabilities of the architecture No other book describes the ARM core from a system and software perspective Author team combines extensive ARM software engineering experience with an in depth knowledge of ARM developer needs Practical executable code is fully explained in the book and available on the publisher s Website Includes a simple embedded operating system

[ARM SYSTEM DEVELOPER'S GUIDE](#) Andrew N Sloss □, 2005-02-28 [Hardware and Computer Organization](#) Arnold S. Berger, 2005-06-08 Hardware and Computer Organization is a practical introduction to the architecture of modern microprocessors This book from the bestselling author explains how PCs work and how to make them work for you It is designed to take students under the hood of a PC and provide them with an understanding of the complex machine that has become such a pervasive part of everyday life It clearly explains how hardware and software cooperatively interact to accomplish real world tasks Unlike other textbooks on this topic Dr Berger s book takes the software developer s point of view Instead of simply demonstrating how to design a computer s hardware it provides an understanding of the total machine highlighting strengths and weaknesses explaining how to deal with memory and how to write efficient assembly code that interacts directly with and takes best advantage of the underlying hardware The book is divided into three major sections Part 1 covers hardware and computer fundamentals including logical gates and simple digital design Elements of hardware development such as instruction set architecture memory and I O organization and analog to digital conversion are examined in detail within the context of modern operating systems Part 2 discusses the software at the lowest level assembly language while Part 3 introduces the reader to modern computer architectures and

reflects on future trends in reconfigurable hardware This book is an ideal reference for ECE software engineering students as well as embedded systems designers professional engineers needing to understand the fundamentals of computer hardware and hobbyists The renowned author s many years in industry provide an excellent basis for the inclusion of extensive real world references and insights Several modern processor architectures are covered with examples taken from each including Intel Motorola MIPS and ARM      *Computer Organization and Design MIPS Edition* David A. Patterson, John L. Hennessy, 2013-09-30 Computer Organization and Design Fifth Edition is the latest update to the classic introduction to computer organization The text now contains new examples and material highlighting the emergence of mobile computing and the cloud It explores this generational change with updated content featuring tablet computers cloud infrastructure and the ARM mobile computing devices and x86 cloud computing architectures The book uses a MIPS processor core to present the fundamentals of hardware technologies assembly language computer arithmetic pipelining memory hierarchies and I O Because an understanding of modern hardware is essential to achieving good performance and energy efficiency this edition adds a new concrete example Going Faster used throughout the text to demonstrate extremely effective optimization techniques There is also a new discussion of the Eight Great Ideas of computer architecture Parallelism is examined in depth with examples and content highlighting parallel hardware and software topics The book features the Intel Core i7 ARM Cortex A8 and NVIDIA Fermi GPU as real world examples along with a full set of updated and improved exercises This new edition is an ideal resource for professional digital system designers programmers application developers and system software developers It will also be of interest to undergraduate students in Computer Science Computer Engineering and Electrical Engineering courses in Computer Organization Computer Design ranging from Sophomore required courses to Senior Electives Winner of a 2014 Texty Award from the Text and Academic Authors Association Includes new examples exercises and material highlighting the emergence of mobile computing and the cloud Covers parallelism in depth with examples and content highlighting parallel hardware and software topics Features the Intel Core i7 ARM Cortex A8 and NVIDIA Fermi GPU as real world examples throughout the book Adds a new concrete example Going Faster to demonstrate how understanding hardware can inspire software optimizations that improve performance by 200 times Discusses and highlights the Eight Great Ideas of computer architecture Performance via Parallelism Performance via Pipelining Performance via Prediction Design for Moore s Law Hierarchy of Memories Abstraction to Simplify Design Make the Common Case Fast and Dependability via Redundancy Includes a full set of updated and improved exercises      Eco-friendly Computing and Communication Systems Jimson Mathew, Priyadarsan Patra, D. K. Pradhan, A.J. Kuttyamma, 2012-07-20 This book constitutes the refereed proceedings of the International Conference Eco friendly Computing and Communication Systems ICECCS 2012 held in Kochi Kerala India in August 2012 The 50 revised full papers presented were carefully reviewed and selected from 133 submissions The papers are organized in topical sections on energy efficient software system

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**Making Intelligent Systems Team Players. A Guide to Developing Intelligent Monitoring Systems** Sherry A. Land,1995

## **Arm System Developer Guide** Book Review: Unveiling the Power of Words

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