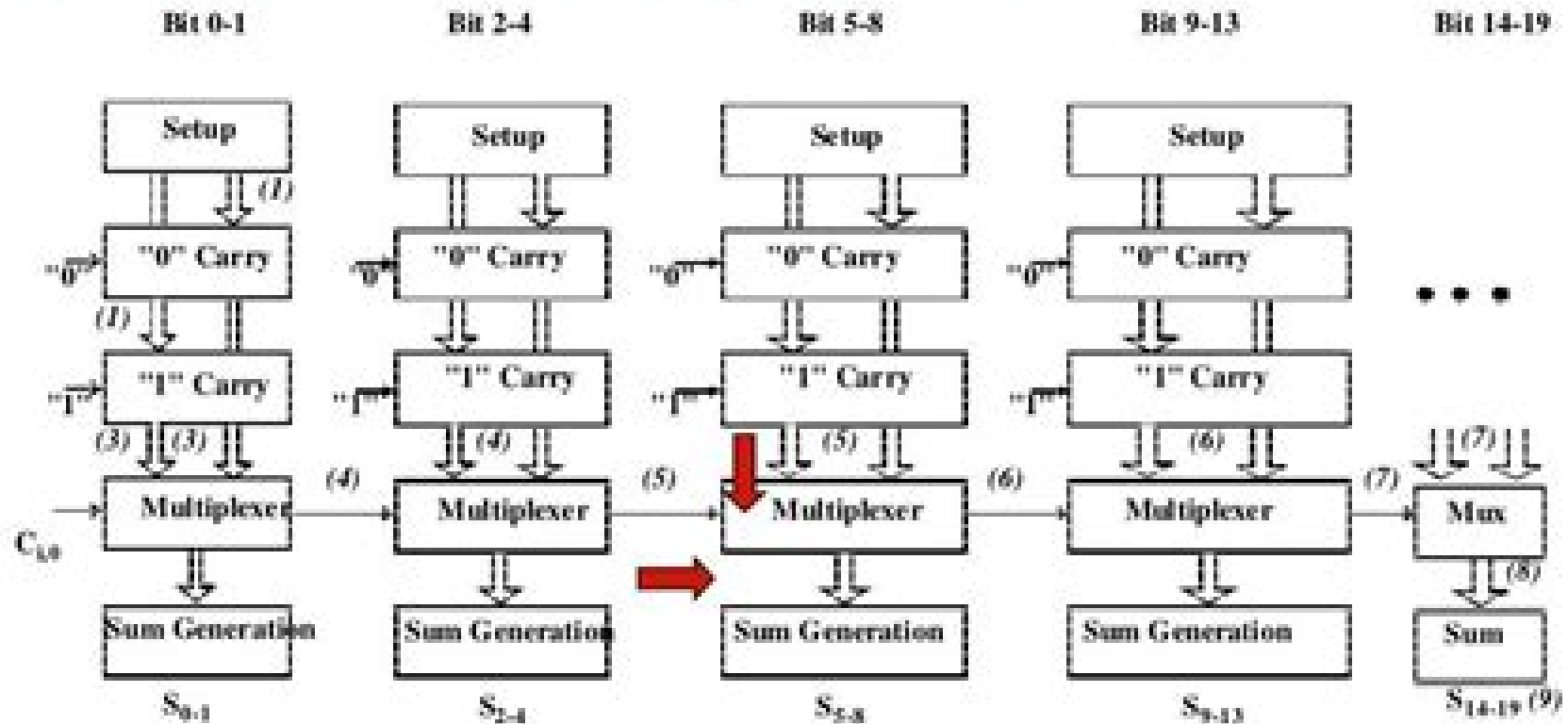


Square Root Carry Select



$$t_{add} = t_{setup} + P \cdot t_{carry} + (\sqrt{2N})t_{mux} + t_{sum}$$

N-bit adder with P stages, 1st stage adds M bits

$$\rightarrow N = P^2 / 2 \Rightarrow P \approx \sqrt{2N}$$

16 Bit Square Root Carry Select Adder

J Elliott



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