

Instruction	Meaning	Addressing mode
LDA #10	Place the decimal value 10 (hex A) into accumulator A.	immediate
LDA \$1000	Copy the contents of the memory location at \$1000 into accumulator A.	extended
LDB #10	Place the decimal value 10 into accumulator B.	immediate
LDB \$1000	Copy the contents of the memory location at \$1000 into accumulator B.	extended
LD #10	Place the decimal value 10 into double accumulator D.	immediate
LD \$1000	Copy the contents of the memory locations at \$1000 & \$1001 into the upper and lower bytes of double accumulator D, respectively.	extended
LDSP #255	Place the decimal value 255 (hex FF) into the stack pointer SP.	immediate
LDSP \$1000	Copy the contents of the memory location at \$1000 and \$1001 into the upper and lower bytes of register SP, respectively.	extended
LDX #51000	Place the hex value \$1000 into index register X.	immediate
LDX \$1000	Copy the contents of the memory locations at \$1000 and \$1001 into the upper and lower bytes of index register X, respectively.	extended
LDY #5000	Place the decimal value 1000 (hex 3E8) into index register Y.	immediate
LDY \$5000	Copy the contents of the memory locations at \$5000 and \$5001 into the upper and lower bytes of index register Y, respectively.	extended

Table 1.2 ■ A sample of LOAD instructions

Example 1.1

Write an instruction to place the decimal value 1023 (or \$3FF) into the stack pointer SP.

Solution: The following instruction will place the decimal value 1023 into the stack pointer SP:

```
LDSP #1023
```

The binary representation of the decimal value 1023 is 1111111111₂. After the execution of this instruction, the 16-bit stack pointer SP contains the value of 0000011111111111₂.

In order to unify the representation, we will use the following notations throughout this book:

[reg]: refers to the contents of the register *reg*. *reg* can be any one of the following:

A, B, D, X, Y, SP, or PC.

[addr]: refers to the contents of the memory location at address *addr*.

mem[addr]: refers to the memory location at address *addr*.

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Dan Black, Leo Chartrand



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The 68HC11 Microcontroller Joseph D. Greenfield,1992 **Technician's Guide to the 68HC11 Microcontroller**
Dan Black,Leo Chartrand,2001 Technician s Guide to the 68HC11 Microcontroller is ideal for readers with little or no prior programming experience who want to master the basics of troubleshooting and programming Motorola s 68HC11 microcontroller Both hardware and software are covered in detail to provide a complete understanding of the principles underlying how a microcontroller works Theory is supplemented by examples designed to illustrate how concepts may be applied By learning how to program the 68HC11 at an elementary level readers also gain valuable experience using manufacturer specific documentation that prepares them for work with other more sophisticated microcontrollers
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