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3 D field simulators and how to apply this knowledge efficiently to EM analysis and design of RF applications in modern communication systems   Proceedings, 2002 International Conference on Advanced Packaging and Systems ,2002

   Analyse Et Caractérisation Des Couplages Substrat Et de la Connectique Dans Les Circuits 3D Fengyuan Sun,2016 The proposal of doubling the number of transistors on an IC chip with minimum costs and subtle innovations every 24 months by Gordon Moore in 1965 the so called called Moore s law has been the most powerful driver for the emphasis of the microelectronics industry in the past 50 years This law enhances lithography scaling and integration in 2D of all functions on a single chip increasingly through system on chip SOC On the other hand the integration of all these functions can be achieved through 3D integrations Generally speaking 3D integration consists of 3D IC packaging 3D IC integration and 3D Si integration They are different and mostly the TSV through silicon via separates 3D IC packaging from 3D IC Si integrations since the latter two uses TSVs but 3D IC packaging does not TSV with a new concept that every chip or interposer could have two surfaces with circuits is the heart of 3D IC Si integrations Continued technology scaling together with the integration of disparate technologies in a single chip means that device performance continues to outstrip interconnect and packaging capabilities and hence there exist many difficult engineering challenges most notably in power management noise isolation and intra and inter chip communication 3D Si integration is the right way to go and compete with Moore s law more than Moore versus more Moore However it is still a long way to go In this book Fengyuan SUN proposes new substrate network extraction techniques Using this latter the substrate coupling and loss in IC s can be analyzed He implements some Green TLM Transmission Line Matrix algorithms in MATLAB It permits to extract impedances between any number of embedded contacts or and TSVS He does investigate models of high aspect ratio TSV on both analytical and numerical methods electromagnetic simulations This model enables to extract substrate and TSV impedance S parameters and parasitic elements considering the variable resistivity of the substrate It is full compatible with SPICE like solvers and should allow an investigation in depth of TSV impact on circuit performance   The Integration and System-wide Implementation of Agilent's VEE Pro Graphical Programming Software with the Cornell Broadband Communicatons Research Laboratory's (CBCRL) Radio Frequency Integrated Chip (RFIC) Test Equipment Christopher Quansah Benyarko,Raymond Ashley Sharp,2003

   Digest IEEE Antennas and Propagation Society. International Symposium,2005   **Electrical Performance of Electronic Packaging** ,2001   Power Integrity Modeling and Design for Semiconductors and Systems Madhavan Swaminathan,A. Ege Engin,2008 Madhavan Swaminathanreceived his B E in Electronics and Communication from Regional Engineering College Tiruchirapalli in 1985 and his M S and Ph D degrees in Electrical Engineering from Syracuse University in 1989 and 1991 respectively He is currently the Joseph M Pettit Professor in Electronics in the School of Electrical and Computer Engineering and Deputy Director of the Packaging Research Center Georgia Tech He is also the cofounder of Jacket Micro Devices a company specializing in RF modules for wireless applications Prior to joining Georgia Tech he was

with IBM where he worked on packaging for super computers His work on Power Integrity has won several awards and he was made an IEEE Fellow for his contributions in this area Ege Enginreceived his B S and M S degrees in electrical engineering from Middle East Technical University Ankara Turkey and from University of Paderborn Germany From 2001 to 2004 he was with the Fraunhofer Institute for Reliability and Microintegration in Berlin During this time he also received his Ph D from the University of Hannover Germany He is currently a Research Engineer in the School of Electrical and Computer Engineering and an Assistant Research Director of the Packaging Research Center at Georgia Tech He has more than 40 publications in refereed journals and conferences in the areas of signal and power integrity modeling and simulation The First Comprehensive Example Rich Guide to Power Integrity Modeling Professionals need to thoroughly understand signal and power integrity issues in order to successfully design packages and boards for high speed systems Now for the first time there s a complete guide to power integrity modeling everything you need to know from the basics through the state of the art Using realistic case studies and downloadable software examples two leading experts demonstrate today s best techniques for designing and modeling interconnects to efficiently distribute power and minimize noise The authors carefully introduce the core concepts of power distribution design systematically present and compare leading techniques for modeling noise and link these techniques to specific applications Their many examples range from the simplest using analytical equations to compute power supply noise through complex system level applications The authors Introduce power delivery network components analysis high frequency measurement and modeling requirements Thoroughly explain plane modeling including plane behavior lumped modeling distributed circuit based approaches and much more Offer in depth coverage of simultaneous switching noise including modeling for time and frequency domain analysis Introduce three leading time domain simulation methods rational function methods signal flow graphs and MNA Present these and other advanced case studies high speed servers high speed differential signaling chip package analysis embedded decoupling capacitors and electromagnetic bandgap structures This book s system level focus and practical examples will make it indispensable for every professional concerned with power integrity including electrical engineers system designers signal integrity engineers and materials scientists It will also be valuable to developers building software that takes advantage of high speed systems During my M S undergraduate days in a little town called Tiruchirapalli in Southern India we used to have frequent voltage and current surges that knocked out all the electrical equipment such as fans and lights in our rooms Frustrated my friend once remarked We are Powerless to solve the Current problem Of course he meant this in jest but little did I realize that this would become the theme of my research for many years Though my area is on Semiconductors and computer system

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