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Advanced Compiler Design And Implementation

David Anthony Greene

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Piguet,2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published Low Power Electronics Design this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software Low Power Processors and Systems on Chips is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts Low Power Processors and Systems on Chips supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing modern computers or embedded systems   FM'99 - Formal Methods Jeannette M. Wing,Jim Woodcook,Jim Davies,2003-07-31 Formal methods are coming of age Mathematical techniques and tools are now regarded as an important part of the development process in a wide range of industrial and governmental organisations A transfer of technology into the mainstream of systems development is slowly but surely taking place FM 99 the First World Congress on Formal Methods in the Development of Computing Systems is a result and a measure of this new found maturity It brings an impressive array of industrial and applications oriented papers that show how formal methods have been used to tackle real problems These proceedings are a record of the technical symposium ofFM 99 alo side the papers describingapplicationsofformalmethods youwill ndtechnical reports papers andabstracts detailing new advances in formaltechniques from mathematical foundations to practical tools The World Congress is the successor to the four Formal Methods Europe Symposia which in turn succeeded the four VDM Europe Symposia This s cession re ects an increasing openness within the international community of researchers and practitioners papers were submitted covering a wide variety of formal methods and application areas The programmecommittee re ects the Congress s international nature with a membership of 84 leading researchersfrom 38 di erent countries The comm tee was divided into 19 tracks each with its own chair to oversee the reviewing process Our collective task was a di cult one there were 259 high quality s missions from 35 di erent countries   Compiler Construction ,2003   **Proceedings of the ... ACM SIGPLAN Symposium on Principles & Practice of Parallel Programming** ,2003   *Languages, Compilers, and Tools for Embedded Systems* ,2000   **UNIX Review's Performance Computing** ,1998-04   *CASES ...* ,2005   **2004 International Conference on Supercomputing** ,2004   **Practical Applications of Parallel Computing** Laurence Tianruo Yang,2003 The continuous

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