

CS501 Advance Computer Architecture - MCQs from Quiz #3

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1. A collection of _____ is called a micro program.

- A. large scale operations
- B. Registers
- C. DMA
- D. Microinstructions

Microinstructions.

2. _____ is the time needed by the CPU to recognize (not service) an interrupt request

- A. Interrupt Latency
- B. Response Deadline
- C. Timer delay
- D. Throughput

Interrupt Latency. (<http://www.vuzs.info/>)

3. _____ controls the sequence of the flow of microinstructions.

- A. Multiplexer
- B. Micro program controller
- C. DMA Controller
- D. Virtual Memory

Micro program controller. (<http://www.vuzs.info/>)

4. Which one of the following is NOT a technique used when the CPU wants to exchange data with a peripheral device?

- A. Direct Memory Access (DMA)
- B. Interrupt driven I/O
- C. Programmed I/O
- D. Virtual Memory

Virtual Memory .

5. Identify the following type of serial communication error condition: "The prior character that was received was not still read by the CPU and is over written by a new received character."

- A. Framing error
- B. Parity error
- C. Overrun error
- D. Under-run error

Overrun error. (<http://www.vuzs.info/>)

6. In which one of the following methods, does the CPU poll to identify the interrupting module and branches to an interrupt service routine on detecting an interrupt?

- A. Daisy Chain
- B. Software Poll
- C. Multiple interrupt lines
- D. All of the given options

Software Poll. (<http://www.vuzs.info/>)

7. An interface that can be used to connect the microcomputer bus to _____ is called an I/O Port.

- A. Flip Flops
- B. Memory

Advance Computer Architecture Objective Questions Answer

Richard Y. Kain

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Table of Contents Advance Computer Architecture Objective Questions Answer

1. Understanding the eBook Advance Computer Architecture Objective Questions Answer
 - The Rise of Digital Reading Advance Computer Architecture Objective Questions Answer
 - Advantages of eBooks Over Traditional Books
2. Identifying Advance Computer Architecture Objective Questions Answer
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Advance Computer Architecture Objective Questions Answer
 - User-Friendly Interface
4. Exploring eBook Recommendations from Advance Computer Architecture Objective Questions Answer
 - Personalized Recommendations
 - Advance Computer Architecture Objective Questions Answer User Reviews and Ratings
 - Advance Computer Architecture Objective Questions Answer and Bestseller Lists
5. Accessing Advance Computer Architecture Objective Questions Answer Free and Paid eBooks
 - Advance Computer Architecture Objective Questions Answer Public Domain eBooks
 - Advance Computer Architecture Objective Questions Answer eBook Subscription Services
 - Advance Computer Architecture Objective Questions Answer Budget-Friendly Options

6. Navigating Advance Computer Architecture Objective Questions Answer eBook Formats
 - ePUB, PDF, MOBI, and More
 - Advance Computer Architecture Objective Questions Answer Compatibility with Devices
 - Advance Computer Architecture Objective Questions Answer Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Advance Computer Architecture Objective Questions Answer
 - Highlighting and Note-Taking Advance Computer Architecture Objective Questions Answer
 - Interactive Elements Advance Computer Architecture Objective Questions Answer
8. Staying Engaged with Advance Computer Architecture Objective Questions Answer
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Advance Computer Architecture Objective Questions Answer
9. Balancing eBooks and Physical Books Advance Computer Architecture Objective Questions Answer
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Advance Computer Architecture Objective Questions Answer
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Advance Computer Architecture Objective Questions Answer
 - Setting Reading Goals Advance Computer Architecture Objective Questions Answer
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Advance Computer Architecture Objective Questions Answer
 - Fact-Checking eBook Content of Advance Computer Architecture Objective Questions Answer
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
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