

CS501 Advance Computer Architecture - MCQS from Quiz #3

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1. A collection of _____ is called a micro program.

- A. large scale operations
 - B. Registers
 - C. DMA
 - D. Microinstructions
- Microinstructions.

2. _____ is the time needed by the CPU to recognize (not service) an interrupt request

- A. Interrupt Latency
- B. Response Deadline
- C. Timer delay
- D. Throughput

Interrupt Latency. (<http://www.vuzs.info/>)

3. _____ controls the sequence of the flow of microinstructions.

- A. Multiplexer
- B. Micro program controller
- C. DMA Controller
- D. Virtual Memory

Micro program controller. (<http://www.vuzs.info/>)

4. Which one of the following is NOT a technique used when the CPU wants to exchange data with a peripheral device?

- A. Direct Memory Access (DMA)
- B. Interrupt driven I/O
- C. Programmed I/O
- D. Virtual Memory

Virtual Memory .

5. Identify the following type of serial communication error condition: "The prior character that was received was not still read by the CPU and is over written by a new received character."

- A. Framing error
- B. Parity error
- C. Overrun error
- D. Under-run error

Overrun error. (<http://www.vuzs.info/>)

6. In which one of the following methods, does the CPU poll to identify the interrupting module and branches to an interrupt service routine on detecting an interrupt?

- A. Daisy Chain
- B. Software Poll
- C. Multiple interrupt lines
- D. All of the given options

Software Poll. (<http://www.vuzs.info/>)

7. An interface that can be used to connect the microcomputer bus to _____ is called an I/O Port.

- A. Flip Flops
- B. Memory

Advance Computer Architecture Objective Questions Answer

Richard Y. Kain



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