

# COLON AND RECTUM STAGING FORM

M0  
 M1  
 M1a  
 M1b

## DISTANT METASTASIS (M)

No distant metastasis (no pathologic M0; use clinical M to complete stage group)

Distant metastasis

Metastasis confined to one organ or site (e.g., liver, lung, ovary, non-regional node).

Metastases in more than one organ/site or the peritoneum.

M1  
 M1a  
 M1b

## ANATOMIC STAGE • PROGNOSTIC GROUPS

CLINICAL						PATHOLOGIC					
GROUP	T	N	M	Dukes'	MAC*	GROUP	T	N	M	Dukes'	MAC*
<input type="checkbox"/> 0	Tis	N0	M0	-	-	<input type="checkbox"/> 0	Tis	N0	M0	-	-
<input type="checkbox"/> I	T1	N0	M0	A	A	<input type="checkbox"/> I	T1	N0	M0	A	A
	T2	N0	M0	A	B1		T2	N0	M0	A	B1
<input type="checkbox"/> II A	T3	N0	M0	B	B2	<input type="checkbox"/> II A	T3	N0	M0	B	B2
<input type="checkbox"/> II B	T4a	N0	M0	B	B2	<input type="checkbox"/> II B	T4a	N0	M0	B	B2
<input type="checkbox"/> II C	T4b	N0	M0	B	B3	<input type="checkbox"/> II C	T4b	N0	M0	B	B3
<input type="checkbox"/> III A	T1-T2	N1/N1c	M0	C	C1	<input type="checkbox"/> III A	T1-T2	N1/N1c	M0	C	C1
	T1	N2a	M0	C	C1		T1	N2a	M0	C	C1
<input type="checkbox"/> III B	T3-T4a	N1/N1c	M0	C	C2	<input type="checkbox"/> III B	T3-T4a	N1/N1c	M0	C	C2
	T2-T3	N2a	M0	C	C1/C2		T2-T3	N2a	M0	C	C1/C2
	T1-T2	N2b	M0	C	C1		T1-T2	N2b	M0	C	C1
<input type="checkbox"/> III C	T4a	N2a	M0	C	C2	<input type="checkbox"/> III C	T4a	N2a	M0	C	C2
	T3-T4a	N2b	M0	C	C2		T3-T4a	N2b	M0	C	C2
	T4b	N1-N2	M0	C	C3		T4b	N1-N2	M0	C	C3
<input type="checkbox"/> IV A	Any T	Any N	M1a	-	-	<input type="checkbox"/> IV A	Any T	Any N	M1a	-	-
<input type="checkbox"/> IV B	Any T	Any N	M1b	-	-	<input type="checkbox"/> IV B	Any T	Any N	M1b	-	-

\*Dukes B is a composite of better (T3 N0 M0) and worse (T4 N0 M0) prognostic groups, as is Dukes C (Any TN1 M0 and Any T N2 M0). MAC is the modified Astler-Coller classification.

Stage unknown

\*Dukes B is a composite of better (T3 N0 M0) and worse (T4 N0 M0) prognostic groups, as is Dukes C (Any TN1 M0 and Any T N2 M0). MAC is the modified Astler-Coller classification.

Stage unknown

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**Richard Bailey**

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web the below figure is the schematic of a simple automatic on off timer with a fixed timing resistor and capacitor so the time period after which this circuit will automatically turn on off the output is fixed and can be found out by using the formula mentioned in the calculation section

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resistors and a capacitor in this circuit no any timer ic is used so the construction of this  
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web jul 9 2023 the following diagram shows a simple on off delay timer circuit in this circuit the timer is a 555 timer chip the relay is a dpdt relay the power supply is a 9v battery the timer is configured in the astable mode this means that it will generate a square wave output

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web jun 5 2022 upon application of input voltage the time delay t1 begins at the end of the time delay t1 the output is energized and remains in that condition for the time delay t2 at the end of this time delay t2 the output is de energized input voltage must be removed to reset the time delay relay

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web dec 31 2020 circuit diagram of the 555 timer latch circuit the schematic of the 555 timer based on off switch is given below in the circuit pin 2 and pin 6 are connected and pins 4 and 8 are also connected the output of the voltage divider circuit is

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web also called normally closed off delay relays one shot timers provide a single contact pulse of specified duration for each coil energization transition from coil off to coil on recycle timers provide a repeating sequence of on off contact pulses as long as the coil is maintained in an energized state

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web jun 26 2023 an on off timer relay circuit diagram is easy to use and understand it is also very versatile allowing for a wide range of applications for example it can be used to control the operation of a variety of devices such as air conditioners fans lighting and other electrical loads

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web this type of positive feedback is also called hysteresis and provides a fast transition from the on to off state once turning off starts the off delay timing is mostly a function of c1 and r6 the exact delay is a bit hard to predict because it depends on the gain of the transistors particularly of q1

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