

Quick reference:

AIA	CMPSB					MOV		
AAD	CMPSW	JAE	JNBE	JPO		MOVSB	RCR	SCASB
AAM	CWD	JB	JNC	JS		MOVSW	REP	SCASW
AAM	DAA	JBE	JNE	JZ		MUL	REPE	SHL
AAS	DAS	JC	JNG	JAHF		NEG	REPNE	SHR
ADC	DEC	JCXZ	JNGE	LDS		NOP	REPNZ	STC
ADD	DIV	JE	JNL	LEA		NOT	REPZ	STD
AND	HLT	JG	JNLE	LES		OR	RET	STI
CALL	IDIV	JGE	JNO	LODSB		OUT	RETF	STOSB
CBW	IMUL	JL	JNP	LODSW		POP	ROL	STOSW
CLC	IN	JLE	JNS	LOOP		POPA	ROR	SUB
CLD	INC	JMP	JNZ	LOOPE		POPF	SAHF	TEST
CLI	INT	JNA	JO	LOOPNE		PUSH	SAL	XCHG
CMC	INTO	JNAE	JP	LOOPNZ		PUSHA	SAR	XLATB
CMP	IRET	JNB	JPE	LOOPZ		PUSHF	SBB	XOR
		JA				RCL		

Operand types:

REG: AX, BX, CX, DX, AH, AL, BL, BH, CH, CL, DH, DL, DI, SI, BP, SP.

SREG: DS, ES, SS, and only as second operand: CS.

memory: [BX], [BX+ SI + 7], variable, etc... (see [Memory Access](#)).

Immediate: 5, -24, 3Fh, 10001101b, etc...

Notes:

- When two operands are required for an instruction they are separated by comma. For example:
REG, memory
- When there are two operands, both operands must have the same size (except shift and rotate instructions). For example:
AL, DL
DX, AX
m1 DB ?
AL, m1
m2 DW ?
AX, m2

- Some instructions allow several operand combinations. For example:

memory, immediate

Assembly Language Instruction Set 8086

George Driver

Assembly Language Instruction Set 8086:

Osborne 16-bit Microprocessor Handbook Adam Osborne, Gerry Kane, 1981 The national semiconductor PACE and INS8900 The general instrument CP 1600 The Texas instruments TMS 9900 TMS 9980 and TMS 9440 products Single chip nova microcomputer central processing units The intel 8086 The zilog Z8000 series Fundamentals of Digital Logic and Microcomputer Design M. Rafiquzzaman, 2005-07-08 Fundamentals of Digital Logic and Microcomputer Design has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers In this Fifth Edition the author focuses on computer design at three levels the device level the logic level and the system level Basic topics are covered such as number systems and Boolean algebra combinational and sequential logic design as well as more advanced subjects such as assembly language programming and microprocessor based system design Numerous examples are provided throughout the text Coverage includes Digital circuits at the gate and flip flop levels Analysis and design of combinational and sequential circuits Microcomputer organization architecture and programming concepts Design of computer instruction sets CPU memory and I/O System design features associated with popular microprocessors from Intel and Motorola Future plans in microprocessor development An instructor's manual available upon request Additionally the accompanying CD ROM contains step by step procedures for installing and using Altera Quartus II software MASM 6.11 8086 and 68asmsim 68000 provides valuable simulation results via screen shots Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems The 8086 Book Russell Rector, George Alexy, 1980

Introduction to Programming in Assembly Language (IBM PC) George Driver, 1993 **Using Assembly Language** Allen L. Wyatt, Sr., Allen Wyatt, 1992 The revised edition to cover the most recent releases of both Microsoft's Macros Assembler and Borland's Turbo Assembler Written from a programmer's perspective this power packed text explains how to use the most popular assemblers linkers and debuggers Includes a comprehensive reference section Some real microprocessors Adam Osborne, Jerry Kane, Gerry Kane, 1978 **Computer Architecture and Organization: From 8085 to core2Duo & beyond** Subrata Ghoshal, 2011 The book uses microprocessors 8085 and above to explain the various concepts and provides additional information about the latest developments like Intel Core II Duo making it one of the most updated textbook in the market **PC Assembly Language Step-by-step** Alex Hoffman, 1990 This important guide teaches readers PC assembly and machine language from the ground up the right way one step at a time Readers will learn at their own pace using the unique simulator which shows how each instruction works as the PC executes it **Assembly Language for Intel-based Computers** Kip R. Irvine, 2007 This widely used fully updated assembly language book provides basic information for the beginning programmer interested in computer architecture operating systems hardware manipulation and compiler writing Uses the Intel IA 32 processor family as its base showing how to program for Windows and DOS Is

written in a clear and straightforward manner for high readability Includes a companion CD ROM with all sample programs and Microsoft^{reg} Macro Assembler Version 8 along with an extensive companion Website maintained by the author Covers machine architecture processor architecture assembly language fundamentals data transfer addressing and arithmetic procedures conditional processing integer arithmetic strings and arrays structures and macros 32 bit Windows programming language interface disk fundamentals BIOS level programming MS DOS programming floating point programming and IA 32 instruction encoding For embedded systems programmers and engineers communication specialists game programmers and graphics programmers Assembly Language Programming and Organization of the IBM PC Ytha Y. Yu,1992

This introduction to the organization and programming of the 8086 family of microprocessors used in IBM microcomputers and compatibles is comprehensive and thorough Includes coverage of I O control video graphics control text display and OS 2 Strong pedagogy with numerous sample programs illustrates practical examples of structured programming

CP/M-86

Assembly Language Programming Jon Lindsay,1986 Details Assembly Language under the CP M 86 Operating System for the Inexperienced User **Assembly Language Tools and Techniques for the IBM Microcomputers** Julio

Sanchez,1990 **Structured Programming in Assembly Language for the IBM PC and PS/2** William C. Runnion,1995 Focusing on ASL for the IBM PC the most popular PC in business and academia this book includes numerous simple to follow examples and code fragments Balanced exercise sets including drill exercises and programming objects provide students with exercises at an appropriate range of difficulty for a variety of skill levels

The 80386, 80486, and Pentium

Processors Walter A. Triebel,1998 This book is the first to concentrate on all 32 bit microprocessors and the pentium This comprehensive exploration of microprocessor technology introduces core concepts techniques and applications using the 80386 80486 and Pentium processors putting equal emphasis on assembly language software programming and microcomputer hardware interfacing The second part of this book presents software memory circuits I O and peripherals The third part consists of PC AT business interfacing testing troubleshooting and the pentium For anyone interested in

Microprocessor Technology IBM Microcomputer Assembly Language J. Terry Godfrey,1989 Software Programming

Languages **First Steps in Assembly Language for the 80286** Robert Erskine,1986 Assembly Language

Step-by-Step Jeff Duntemann,2000 The bestselling guide to assembly language now updated and expanded to include coverage of Linux This new edition of the bestselling guide to assembly programming now covers DOS and Linux The Second Edition begins with a highly accessible overview of the internal operations of the Intel based PC and systematically covers all the steps involved in writing testing and debugging assembly programs Expert author Jeff Duntemann then presents working example programs for both the DOS and Linux operating systems using the popular free assembler NASM He also includes valuable information on how to use procedures and macros plus rare explanations of assembly level coding for Linux all of which combine to offer a comprehensive look at the complexities of assembly programming for Intel processors Providing you

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