

8.10 Timer/Counter Oscillator

Atmel® ATmega328P uses the same crystal oscillator for low-frequency oscillator and Timer/Counter oscillator. See [Section 8.5 “Low Frequency Crystal Oscillator” on page 29](#) for details on the oscillator and crystal requirements.

Atmel ATmega328P share the Timer/Counter oscillator pins (TOSC1 and TOSC2) with XTAL1 and XTAL2. When using the Timer/Counter oscillator, the system clock needs to be four times the oscillator frequency. Due to this and the pin sharing, the Timer/Counter oscillator can only be used when the calibrated internal RC oscillator is selected as system clock source.

Applying an external clock source to TOSC1 can be done if EXTCLK in the ASSR register is written to logic one. See [Section 17.9 “Asynchronous Operation of Timer/Counter2” on page 126](#) for further description on selecting external clock as input instead of a 32.768kHz watch crystal.

8.11 System Clock Prescaler

The Atmel ATmega328P has a system clock prescaler, and the system clock can be divided by setting the [Section 8.12.2 “CLKPR – Clock Prescale Register” on page 33](#). This feature can be used to decrease the system clock frequency and the power consumption when the requirement for processing power is low. This can be used with all clock source options, and it will affect the clock frequency of the CPU and all synchronous peripherals. clk_{IO} , clk_{ADC} , clk_{CPU} , and clk_{FLASH} are divided by a factor as shown in [Table 28-4 on page 261](#).

When switching between prescaler settings, the system clock prescaler ensures that no glitches occurs in the clock system. It also ensures that no intermediate frequency is higher than neither the clock frequency corresponding to the previous setting, nor the clock frequency corresponding to the new setting. The ripple counter that implements the prescaler runs at the frequency of the undivided clock, which may be faster than the CPU's clock frequency. Hence, it is not possible to determine the state of the prescaler - even if it were readable, and the exact time it takes to switch from one clock division to the other cannot be exactly predicted. From the time the CLKPS values are written, it takes between $T1 + T2$ and $T1 + 2 \times T2$ before the new clock frequency is active. In this interval, 2 active clock edges are produced. Here, $T1$ is the previous clock period, and $T2$ is the period corresponding to the new prescaler setting.

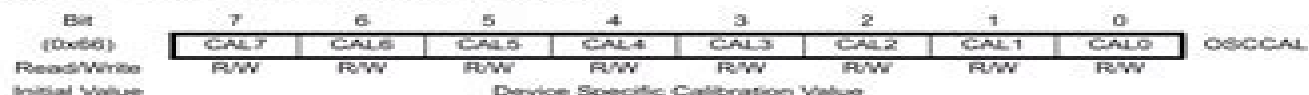
To avoid unintentional changes of clock frequency, a special write procedure must be followed to change the CLKPS bits:

1. Write the clock prescaler change enable (CLKPCE) bit to one and all other bits in CLKPR to zero.
2. Within four cycles, write the desired value to CLKPS while writing a zero to CLKPCE.

Interrupts must be disabled when changing prescaler setting to make sure the write procedure is not interrupted.

8.12 Register Description

8.12.1 OSCCAL – Oscillator Calibration Register



• Bits 7..0 – CAL7..0: Oscillator Calibration Value

The oscillator calibration register is used to trim the calibrated internal RC oscillator to remove process variations from the oscillator frequency. A pre-programmed calibration value is automatically written to this register during chip reset, giving the factory calibrated frequency as specified in [Table 28-1 on page 260](#). The application software can write this register to change the oscillator frequency. The oscillator can be calibrated to frequencies as specified in [Table 28-1 on page 260](#). Calibration outside that range is not guaranteed.

Note that this oscillator is used to time EEPROM and flash write accesses, and these write times will be affected accordingly. If the EEPROM or flash are written, do not calibrate to more than 8.8MHz. Otherwise, the EEPROM or flash write may fail.

The CAL7 bit determines the range of operation for the oscillator. Setting this bit to 0 gives the lowest frequency range, setting this bit to 1 gives the highest frequency range. The two frequency ranges are overlapping, in other words a setting of $OSCCAL = 0x7F$ gives a higher frequency than $OSCCAL = 0x80$.

The CAL6..0 bits are used to tune the frequency within the selected range. A setting of 0x00 gives the lowest frequency in that range, and a setting of 0x7F gives the highest frequency in the range.

Atmel Frequency Counter

G Orfield



Atmel Frequency Counter:

Atmel AVR Microcontroller Primer Steven Barrett, Daniel Pack, 2022-11-10 This textbook provides practicing scientists and engineers a primer on the Atmel AVR microcontroller In this second edition we highlight the popular ATmega164 microcontroller and other pin for pin controllers in the family with a complement of flash memory up to 128 kbytes The second edition also adds a chapter on embedded system design fundamentals and provides extended examples on two different autonomous robots Our approach is to provide the fundamental skills to quickly get up and operating with this internationally popular microcontroller We cover the main subsystems aboard the ATmega164 providing a short theory section followed by a description of the related microcontroller subsystem with accompanying hardware and software to exercise the subsystem In all examples we use the C programming language We include a detailed chapter describing how to interface the microcontroller to a wide variety of input and output devices and conclude with several system level examples Table of Contents Atmel AVR Architecture Overview Serial Communication Subsystem Analog to Digital Conversion Interrupt Subsystem Timing Subsystem Atmel AVR Operating Parameters and Interfacing Embedded Systems Design **Embedded Systems Design with the Atmel AVR Microcontroller** Steven F. Barrett, 2010 This textbook provides practicing scientists and engineers an advanced treatment of the Atmel AVR microcontroller This book is intended as a follow on to a previously published book titled Atmel AVR Microcontroller Primer Programming and Interfacing Some of the content from this earlier text is retained for completeness This book will emphasize advanced programming and interfacing skills We focus on system level design consisting of several interacting microcontroller subsystems The first chapter discusses the system design process Our approach is to provide the skills to quickly get up to speed to operate the internationally popular Atmel AVR microcontroller line by developing systems level design skills We use the Atmel ATmega164 as a representative sample of the AVR line The knowledge you gain on this microcontroller can be easily translated to every other microcontroller in the AVR line In succeeding chapters we cover the main subsystems aboard the microcontroller providing a short theory section followed by a description of the related microcontroller subsystem with accompanying software for the subsystem We then provide advanced examples exercising some of the features discussed In all examples we use the C programming language The code provided can be readily adapted to the wide variety of compilers available for the Atmel AVR microcontroller line We also include a chapter describing how to interface the microcontroller to a wide variety of input and output devices The book concludes with several detailed system level design examples employing the Atmel AVR microcontroller *Embedded System Design with the Atmel AVR Microcontroller II* Steven Barrett, 2022-05-31 This textbook provides practicing scientists and engineers an advanced treatment of the Atmel AVR microcontroller This book is intended as a follow on to a previously published book titled Atmel AVR Microcontroller Primer Programming and Interfacing Some of the content from this earlier text is retained for completeness This book will emphasize advanced programming and interfacing skills We focus on system

level design consisting of several interacting microcontroller subsystems The first chapter discusses the system design process Our approach is to provide the skills to quickly get up to speed to operate the internationally popular Atmel AVR microcontroller line by developing systems level design skills We use the Atmel ATmega164 as a representative sample of the AVR line The knowledge you gain on this microcontroller can be easily translated to every other microcontroller in the AVR line In succeeding chapters we cover the main subsystems aboard the microcontroller providing a short theory section followed by a description of the related microcontroller subsystem with accompanying software for the subsystem We then provide advanced examples exercising some of the features discussed In all examples we use the C programming language The code provided can be readily adapted to the wide variety of compilers available for the Atmel AVR microcontroller line We also include a chapter describing how to interface the microcontroller to a wide variety of input and output devices The book concludes with several detailed system level design examples employing the Atmel AVR microcontroller

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Arduino Microcontroller Processing for Everyone! Part II Steven Barrett, 2022-11-10 This book is about the Arduino microcontroller and the Arduino concept The visionary Arduino team of Massimo Banzi David Cuartielles Tom Igoe Gianluca Martino and David Mellis launched a new innovation in microcontroller hardware in 2005 the concept of open source hardware Their approach was to openly share details of microcontroller based hardware design platforms to stimulate the sharing of ideas and promote innovation This concept has been popular in the software world for many years This book is intended for a wide variety of audiences including students of the fine arts middle and senior high school students engineering design students and practicing scientists and engineers To meet this wide audience the book has been divided into sections to satisfy the need of each reader The book contains many software and hardware examples to assist the reader in developing a wide variety of systems For the examples the Arduino Duemilanove and the Atmel ATmega328 is employed as the target processor

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