System Information | **Status Monitor** | **Performance Control** | **Preference**

Basic | Detailed | Diagram

Processor

Name	AMD Phenom(tm) II X4 955 Processor
Instructions	MMX(+), 3DNow!™(+), SSE, SSE2, SSE3, SSE4A, x86-64

Family	10	Model	04	Stepping	2
Package	Socket AM3	Revision	RB-C2		
HT ref. Clock	200.68 MHz	Voltage	1.35 V		
Core Speed	3210.87 MHz	Core Multiplier	16.00 X		
NB Speed	2408.22 MHz	NB Multiplier	12.00 X		

Cache

L1 Code Cache	64 KB X 4
L1 Data Cache	64 KB X 4
Level 2 Cache	512 KB X 4
Level 3 Cache	6144 KB

HyperTransport™ Link

Speed	2006.79 MHz
Max Width In	16 bits
Max Width Out	16 bits
Width In	16 bits
Width Out	16 bits

SPD

Slot	Slot 2
Capacity	2048 MB
Type	DDR3 SDRAM
Max Bandwidth	8.5 GB/s
Manufacture	Corsair
Part Number	CMD12G1600C1DHX
Serial Number	0
Date	Week 0 Year 2000

Freq	606 MHz	610 MHz	533 MHz
Trp	9 clock	8 clock	7 clock
Trcd	6 clock	5 clock	4 clock
Trcd	9 clock	8 clock	7 clock
Tras	26 clock	23 clock	20 clock
Twr	11 clock	10 clock	8 clock
Twtr	6 clock	5 clock	4 clock
Trtp	6 clock	5 clock	4 clock
Trc	35 clock	31 clock	27 clock
Td	9 clock	8 clock	7 clock

Memory

Capacity	4096 MB	Frequency	802.74 MHz
Type	DDR3 SDRAM	Channel	Dual
CAS Latency	9 clock	RAS to CAS Delay	9 clock
Command Rate	1T	RAS Active Time	24 clock
Row Cycle Time	34 clock	Row Precharge Time	9 clock

OK | Cancel | Apply | Discard

AMD

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Christian Drosten

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